

Fig. 1

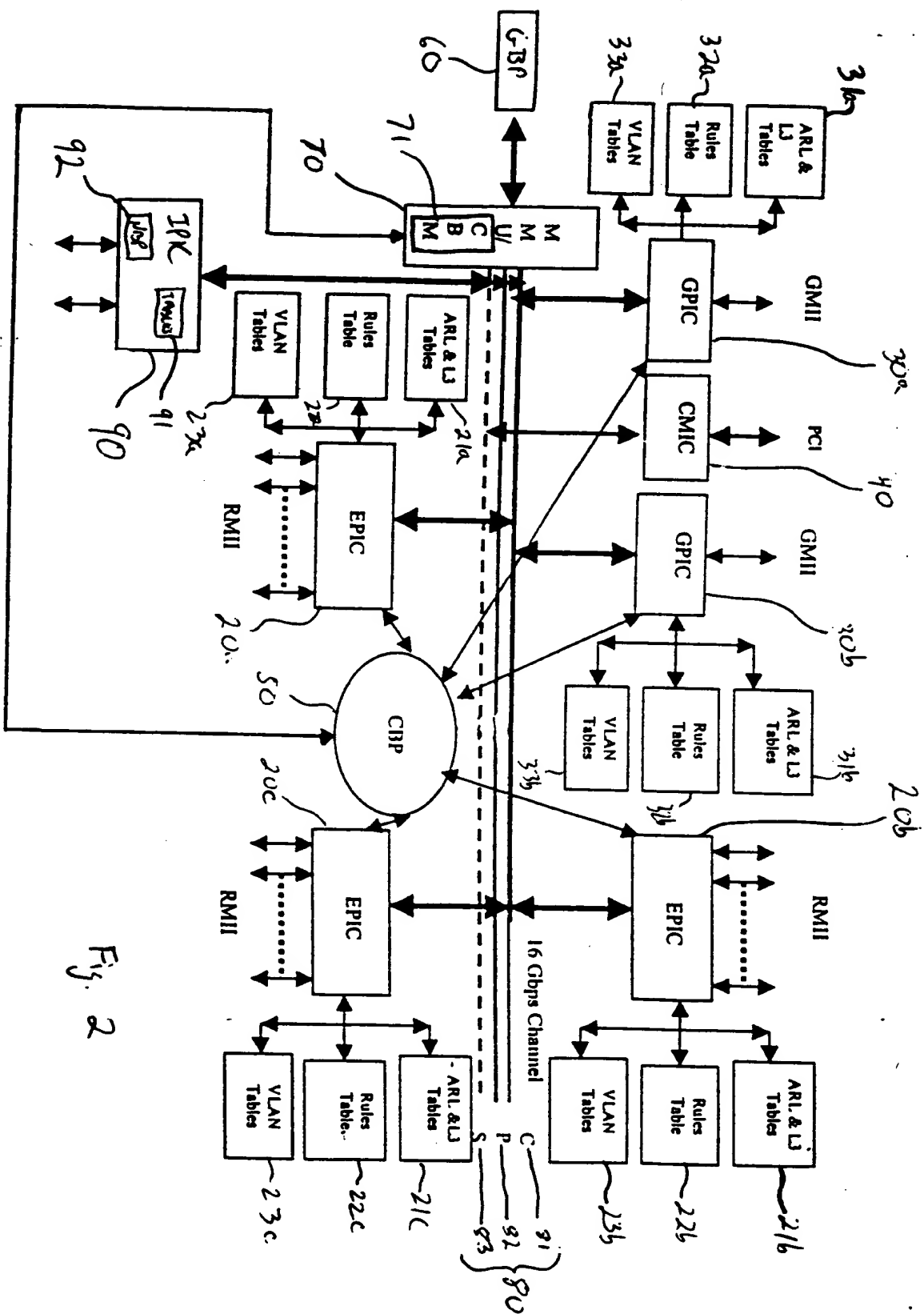


Fig. 2

Locked and
sync to each
other

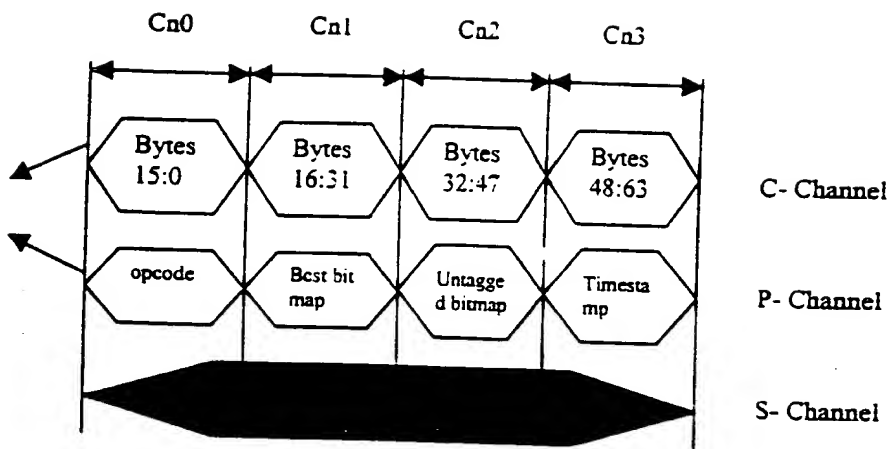


Fig. 3

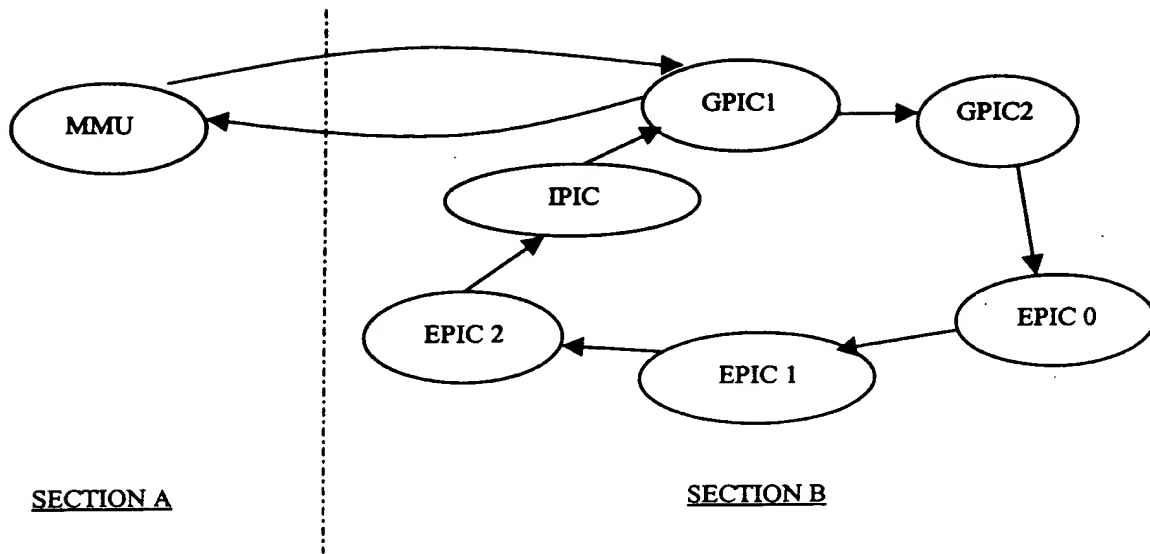


Fig. 4a

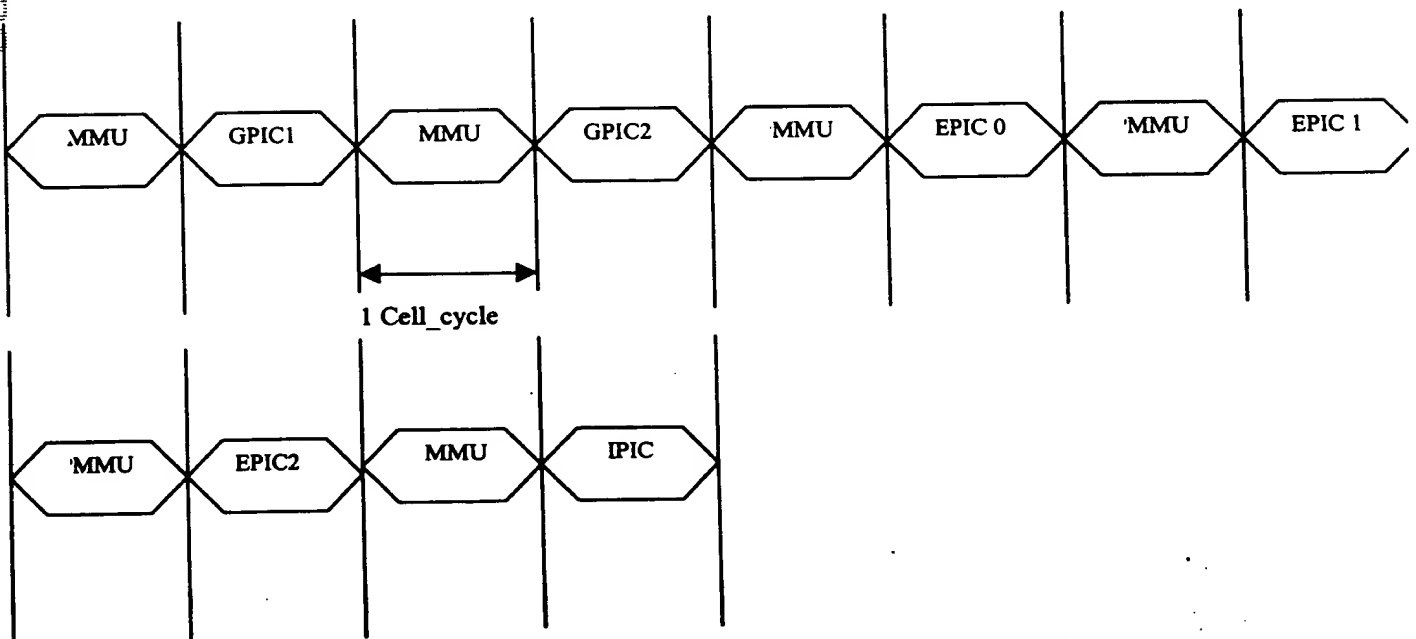


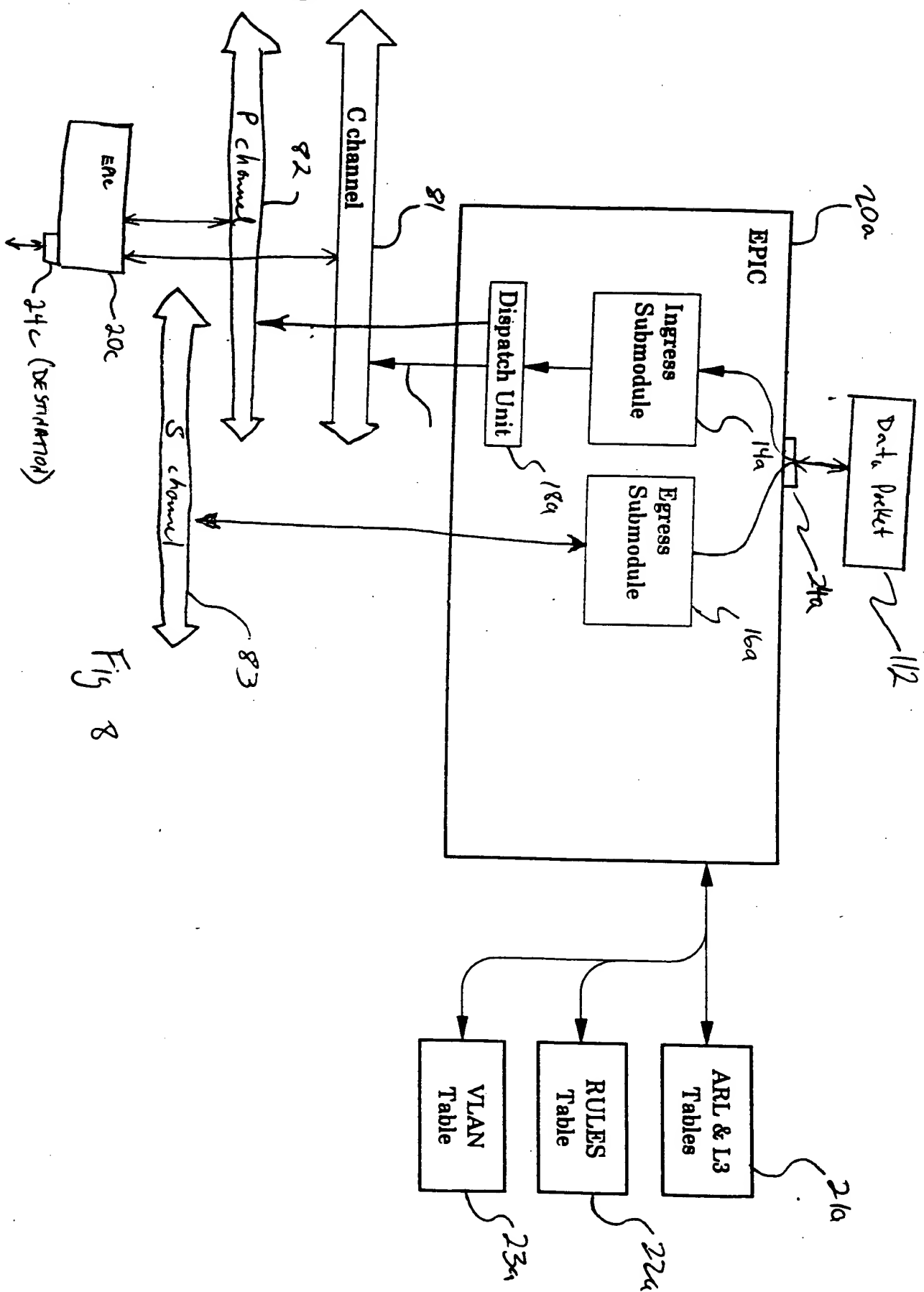
Fig. 4b

62	60	58	56	54	52	50	48	46	44	42	40	38	36	34	32
R	L3 Port Bitmap														

Fig. 5

[illegible]

Fig. 6.



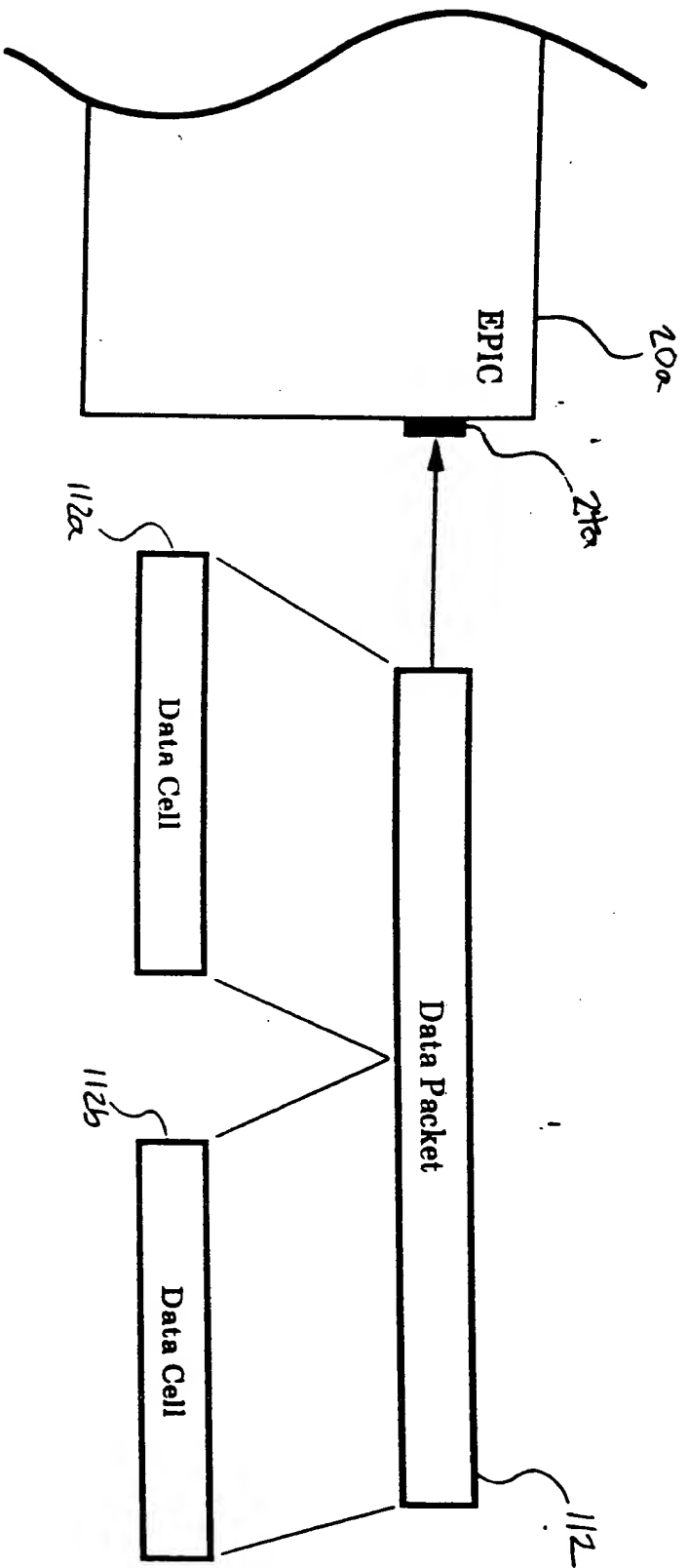


Fig. 9

X_VAC

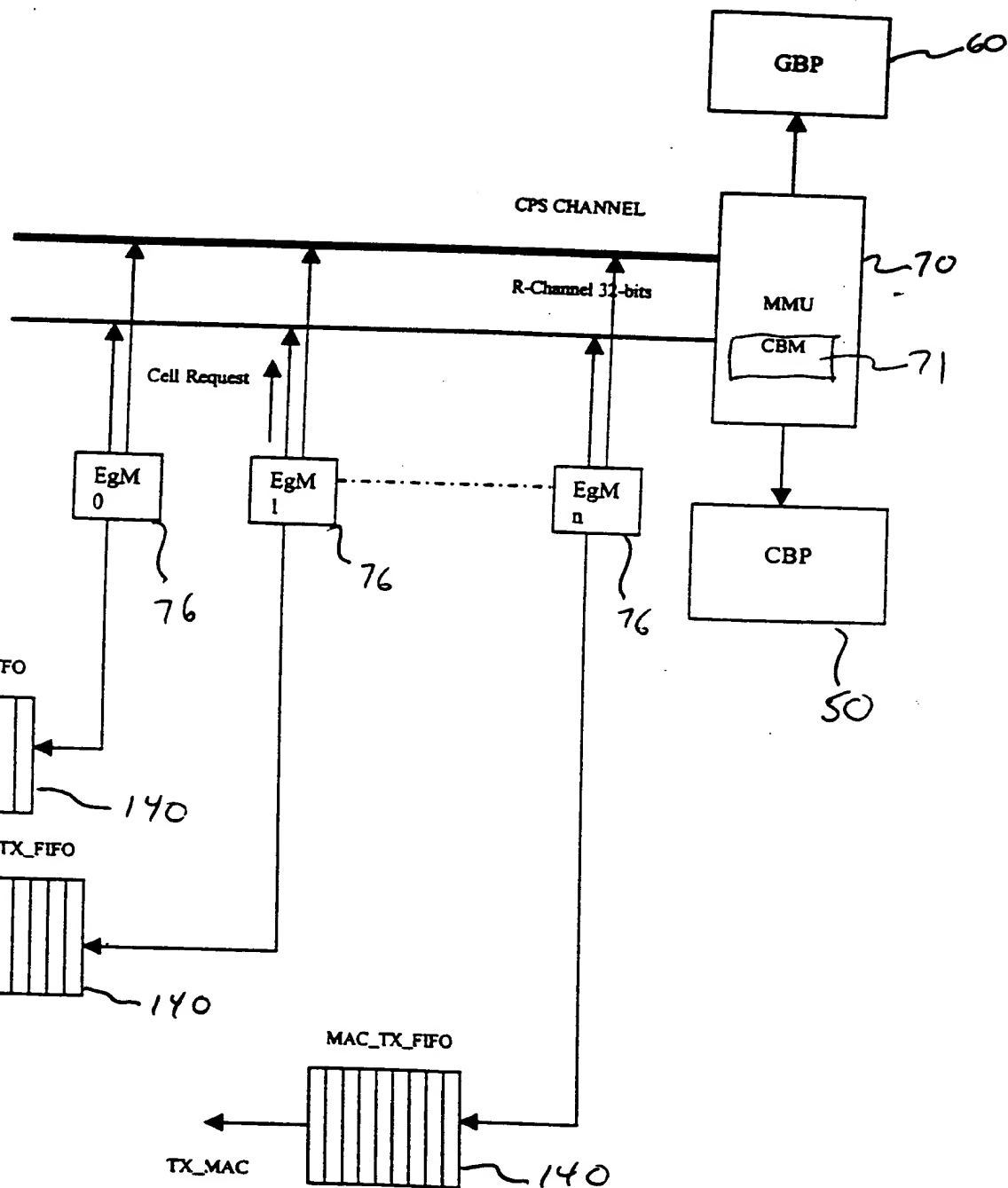


Fig. 10

Line 0 →	FC LC BC/MC Cpy_cnt(5b) Cell_length (7b) CRC (2b) NC_header (16b) Src Count(6) IPX IP Time_Stamp (14b) O bits(2b) P NextCellLen(2b) CpuOpcode(4b) Cell_data (0-9B)
Line 1 →	
Line 2 →	Cell_data (10-27) Bytes
Line 3 →	Cell_data (28-45) Bytes
	Cell_data (46-63) Bytes

Fig. 11

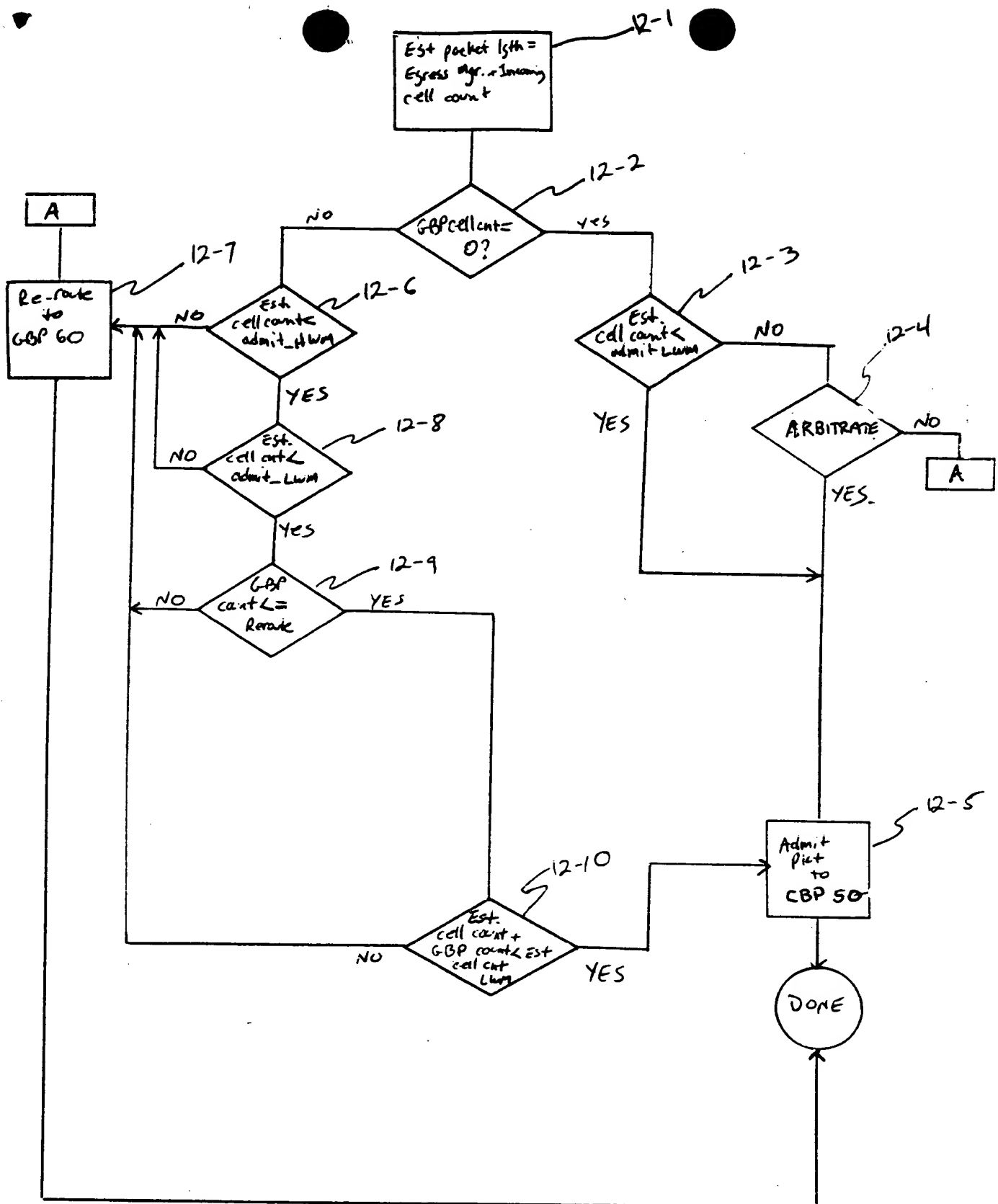
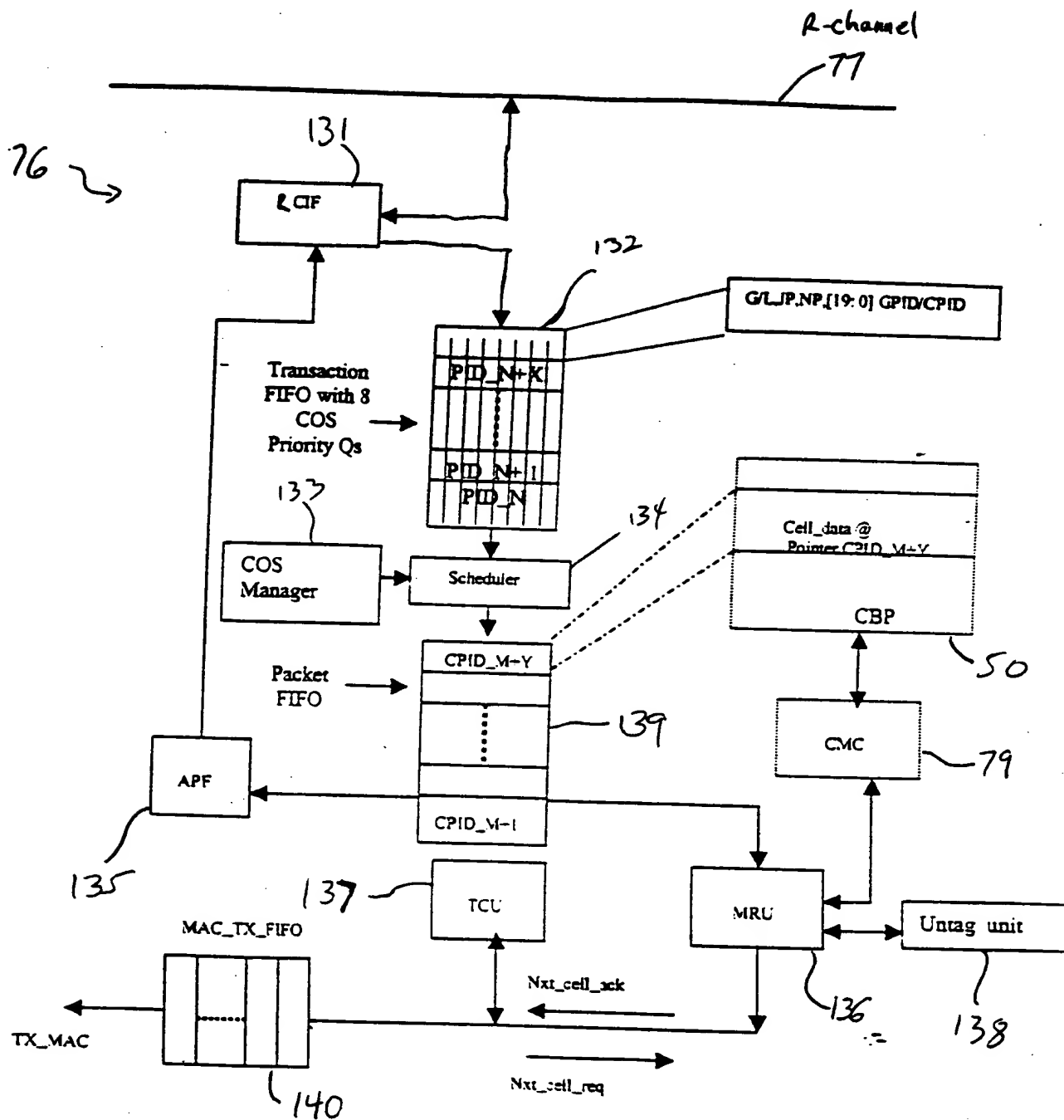


Fig. 12

R-channel



Data Flow

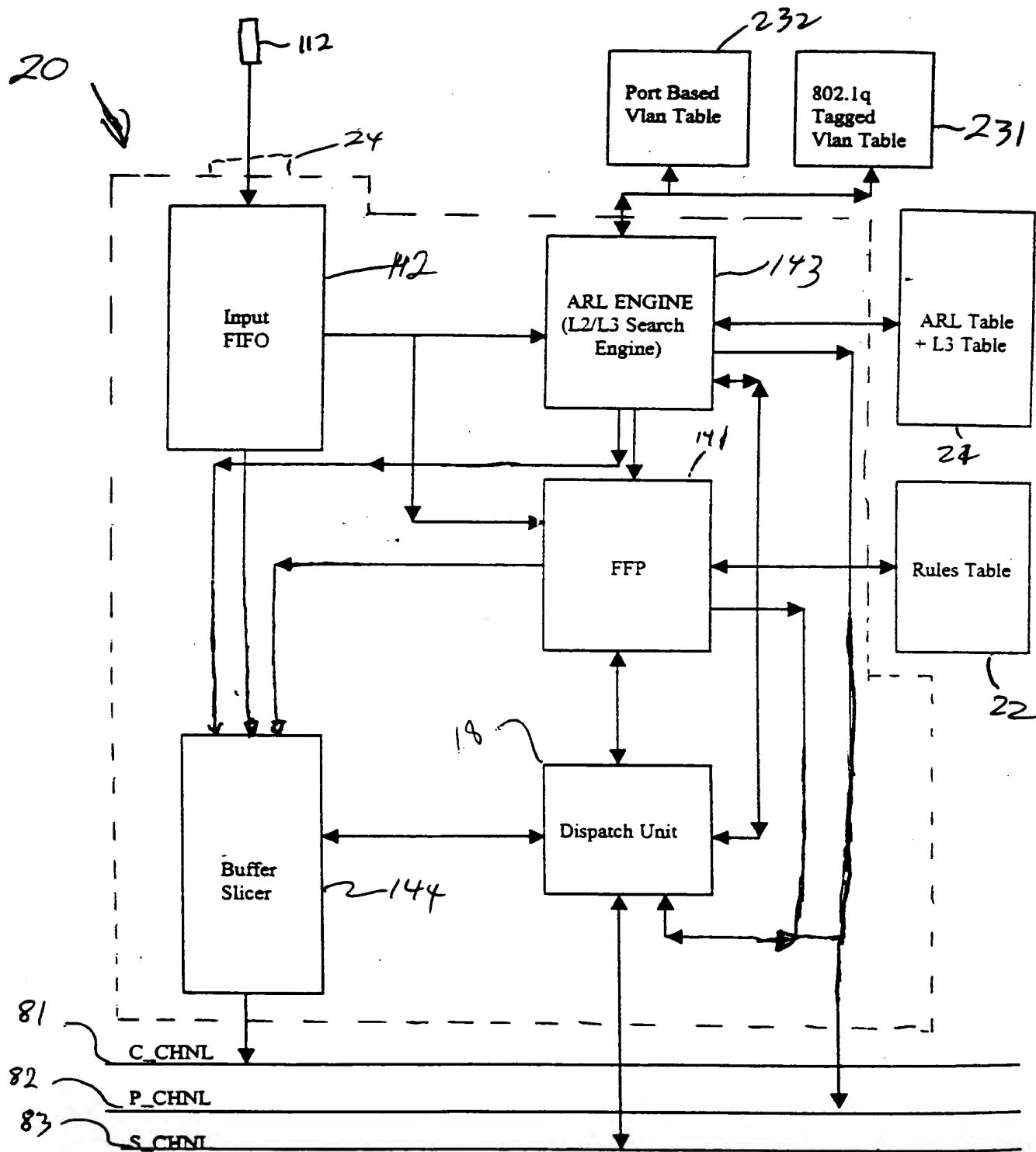


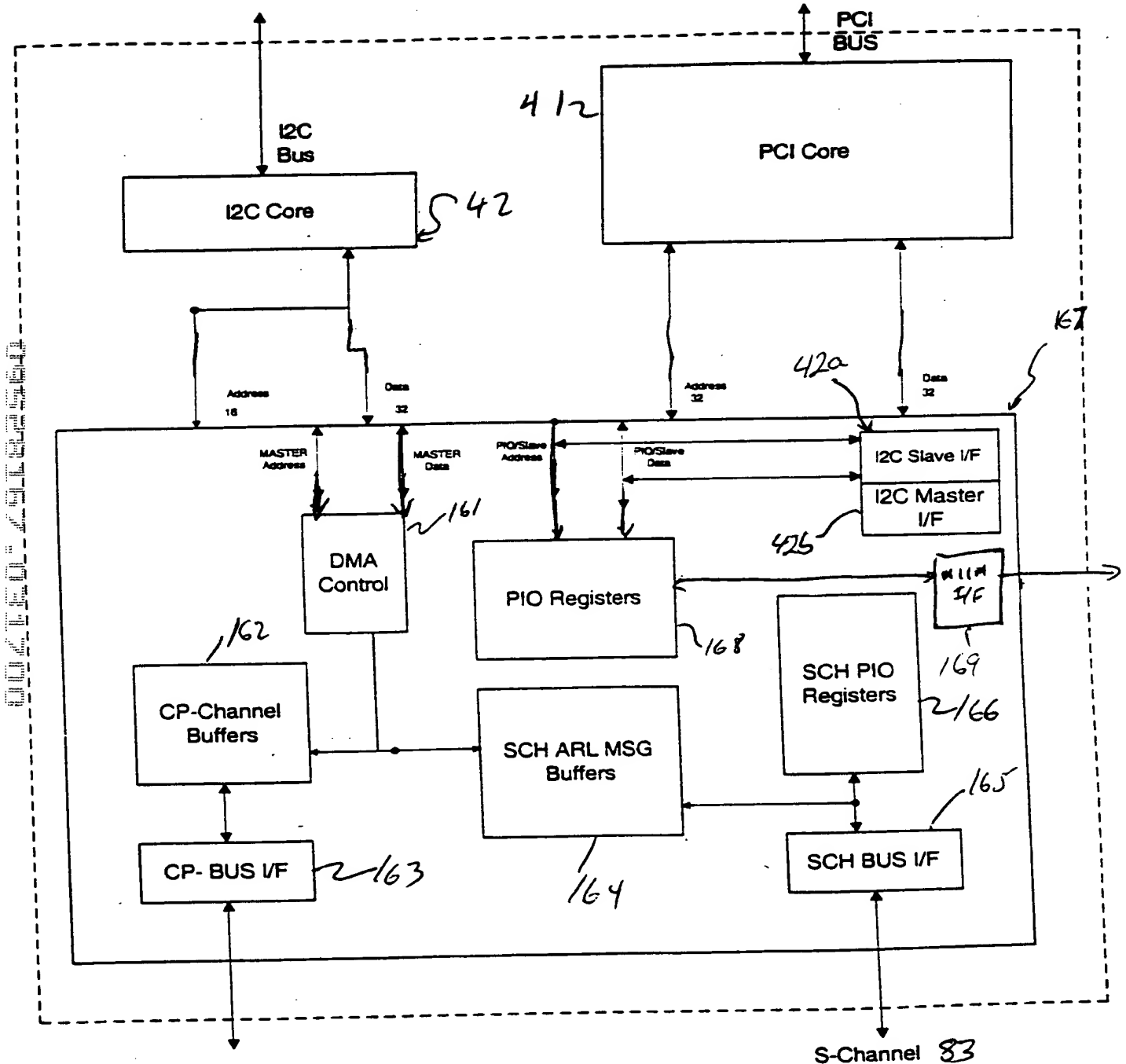
Fig. 14

140



FIG. 15

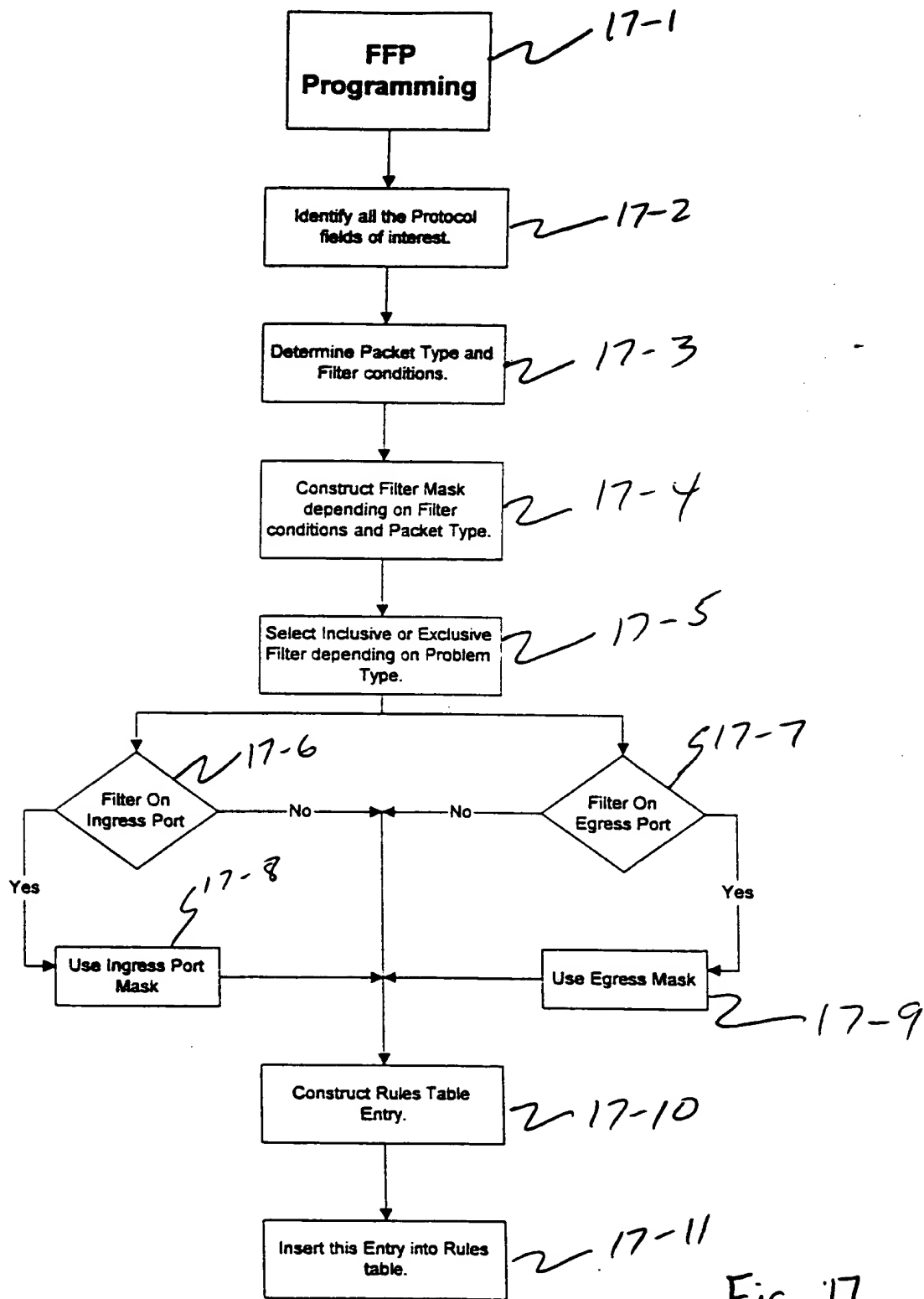
40



C channel 81
P channel 82

Fig. 16

FFP Programming Flow Chart



Age Timer Expired ->
Start ARL Aging
Process;
Start with the first ARL
Entry

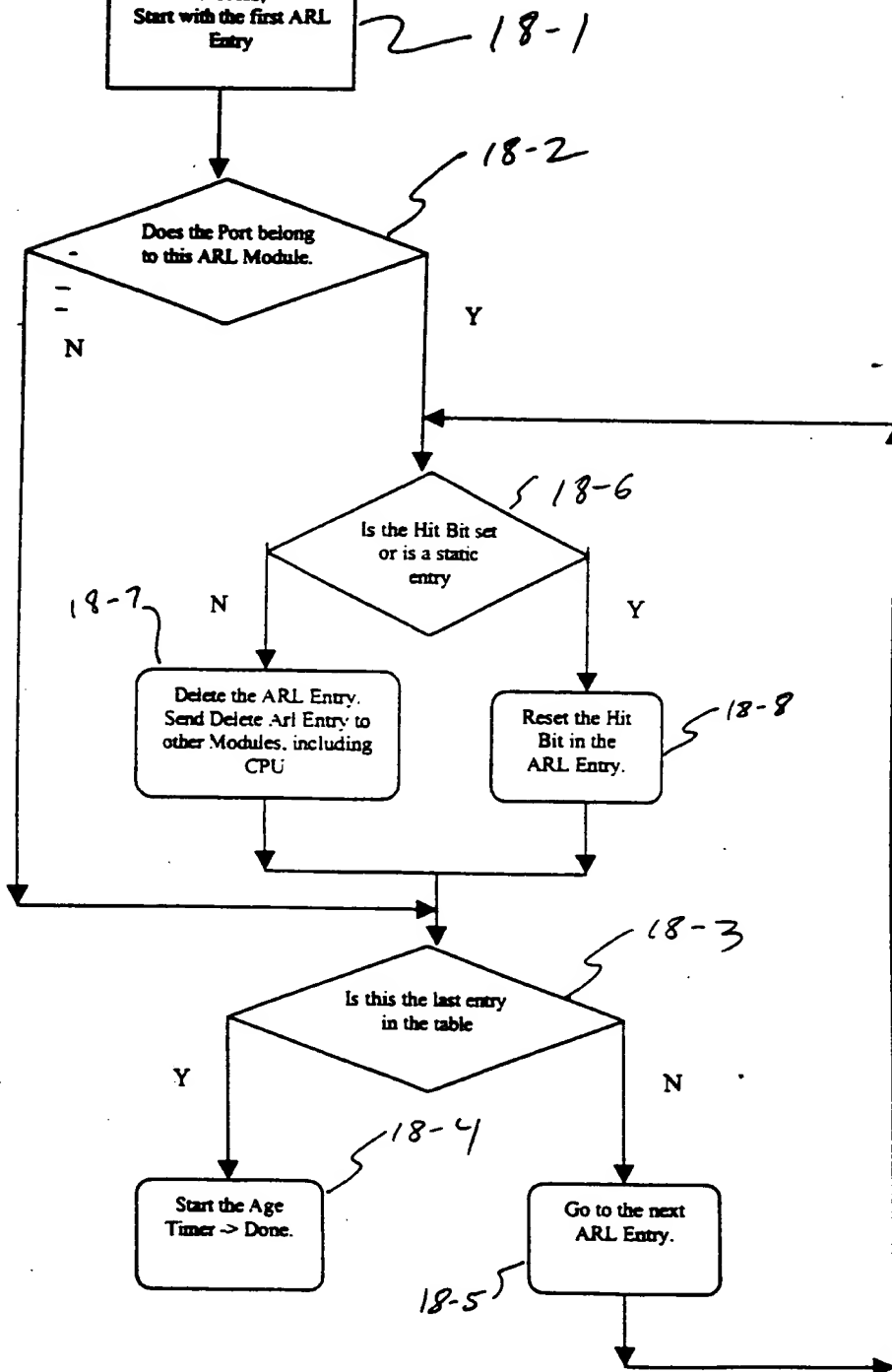


Fig. 18

Variable	Mean	SD	Min	Max
Age	34.5	10.2	22	55
Gender	Male	Female		
Marital status	Married	Single		
Education	High school	College		
Occupation	Manager	Worker		
Income	Low	High		
Health status	Good	Poor		
Stress level	Low	High		
Life satisfaction	Low	High		
Resilience	Low	High		
Optimism	Low	High		
Self-efficacy	Low	High		
Perceived stress	Low	High		
Depression	Low	High		
Anxiety	Low	High		
Quality of life	Low	High		
Physical health	Low	High		
Mental health	Low	High		
Social support	Low	High		
Life events	Low	High		
Personal growth	Low	High		
Meaning in life	Low	High		
Existential well-being	Low	High		
Psychological well-being	Low	High		
Life satisfaction	Low	High		
Overall health	Low	High		

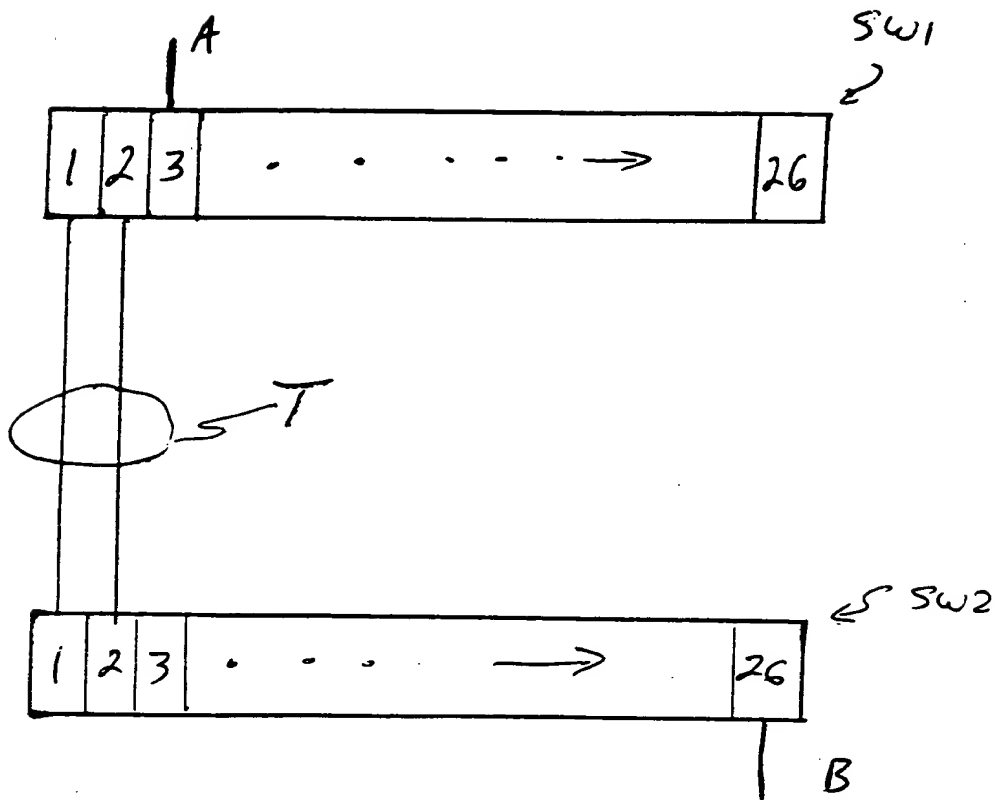


Fig. 19

Figure 20

Fig. 21a

Filter Mask Format:

Filter Enable (1b)	Counter (5b)	Rem Port (1b)	Output Mod (5b)	Output Port (6b)	TOS Prec (3b)	Diff Serv (6b)	802.1p Prior (3b)	
NMA Enb (1b)	No Match Action (10b)	Data Offset 4 (7b)	Data Offset 3 (7b)	Data Offset 2 (7b)	Data Offset 1 (7b)	Ingress Port Mask (6b)	Egress ModId Mask (5b)	Egress Port Mask (6b)
Field Mask								

Field Mask Format:

Dest Mac addr (6 B)	Src Mac addr (6 B)	Prot type (2 B)	Dest SAP (1 B)	Src SAP (1 B)	802.1 p Prio (3 b)	Vlan Id (12b)	TOS Prec (3b)	Diff Serv (6b)	Src IP addr (4B)	Dest IP addr (4 B)	Prot IP- (1B)	Src Port (2B)	Dest Port (2B)					
<table><tr><td>TCP Cntr Flags (1B)</td><td>Data 1 (8B)</td><td>Data 2 (8 B)</td><td>Data 3 (8B)</td><td>Data 4 (8B)</td></tr></table>														TCP Cntr Flags (1B)	Data 1 (8B)	Data 2 (8 B)	Data 3 (8B)	Data 4 (8B)
TCP Cntr Flags (1B)	Data 1 (8B)	Data 2 (8 B)	Data 3 (8B)	Data 4 (8B)														

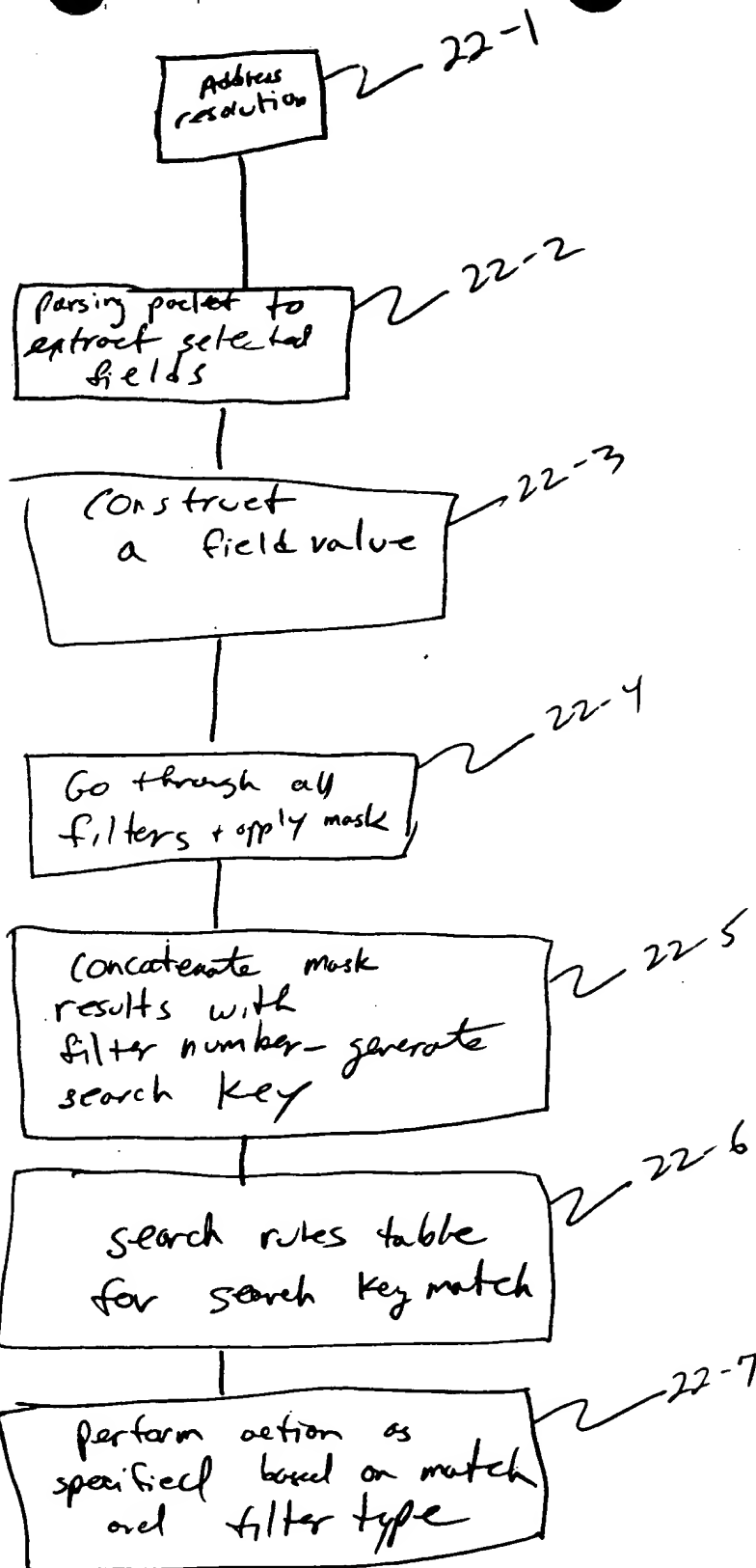


Fig. 22

Variable	Mean	SD	Min	Max
Age	34.5	10.2	21	55
Gender	0.5	0.5	0	1
Marital status	0.6	0.5	0	1
Education	12.5	1.5	9	16
Income	15.2	8.5	5	35
Occupation	1.2	0.8	0	2
Health status	1.5	0.5	1	2
Stress level	2.5	1.2	1	4
Life satisfaction	3.5	1.5	1	5
Resilience	4.5	1.5	1	6
Optimism	5.5	1.5	1	7
Gratitude	6.5	1.5	1	8
Forgiveness	7.5	1.5	1	9
Empathy	8.5	1.5	1	10
Compassion	9.5	1.5	1	11
Kindness	10.5	1.5	1	12
Generosity	11.5	1.5	1	13
Patience	12.5	1.5	1	14
Self-control	13.5	1.5	1	15
Perseverance	14.5	1.5	1	16
Determination	15.5	1.5	1	17
Confidence	16.5	1.5	1	18
Trust	17.5	1.5	1	19
Cooperation	18.5	1.5	1	20
Teamwork	19.5	1.5	1	21
Leadership	20.5	1.5	1	22
Influence	21.5	1.5	1	23
Power	22.5	1.5	1	24
Authority	23.5	1.5	1	25
Control	24.5	1.5	1	26
Management	25.5	1.5	1	27
Organization	26.5	1.5	1	28
Planning	27.5	1.5	1	29
Execution	28.5	1.5	1	30
Monitoring	29.5	1.5	1	31
Evaluation	30.5	1.5	1	32
Reflection	31.5	1.5	1	33
Learning	32.5	1.5	1	34
Growth	33.5	1.5	1	35
Development	34.5	1.5	1	36
Progress	35.5	1.5	1	37
Success	36.5	1.5	1	38
Achievement	37.5	1.5	1	39
Realization	38.5	1.5	1	40
Fulfillment	39.5	1.5	1	41
Completion	40.5	1.5	1	42
Conclusion	41.5	1.5	1	43
Summary	42.5	1.5	1	44
Final	43.5	1.5	1	45
End	44.5	1.5	1	46
Close	45.5	1.5	1	47
Stop	46.5	1.5	1	48
Quit	47.5	1.5	1	49
Leave	48.5	1.5	1	50
Exit	49.5	1.5	1	51
Depart	50.5	1.5	1	52
Go	51.5	1.5	1	53
Move	52.5	1.5	1	54
Transfer	53.5	1.5	1	55
Relocate	54.5	1.5	1	56
Shift	55.5	1.5	1	57
Change	56.5	1.5	1	58
Alter	57.5	1.5	1	59
Modify	58.5	1.5	1	60
Adjust	59.5	1.5	1	61
Tweak	60.5	1.5	1	62
Fix	61.5	1.5	1	63
Repair	62.5	1.5	1	64
Maintain	63.5	1.5	1	65
Preserve	64.5	1.5	1	66
Protect	65.5	1.5	1	67
Defend	66.5	1.5	1	68
Guard	67.5	1.5	1	69
Watch	68.5	1.5	1	70
Observe	69.5	1.5	1	71
Monitor	70.5	1.5	1	72
Track	71.5	1.5	1	73
Follow	72.5	1.5	1	74
Trace	73.5	1.5	1	75
Trace	74.5	1.5	1	76
Trace	75.5	1.5	1	77
Trace	76.5	1.5	1	78
Trace	77.5	1.5	1	79
Trace	78.5	1.5	1	80

[illegible]

Fig. 23

30	28	26	24	22	20	18	16	14	12	10	8	6	4	2	0
Source IP Address															
Multicast IP Address															
r	L3 Port Bitmap														
L3 Module Bitmap															
Unused											TTL Threshold		Source Port		

Fig. 24

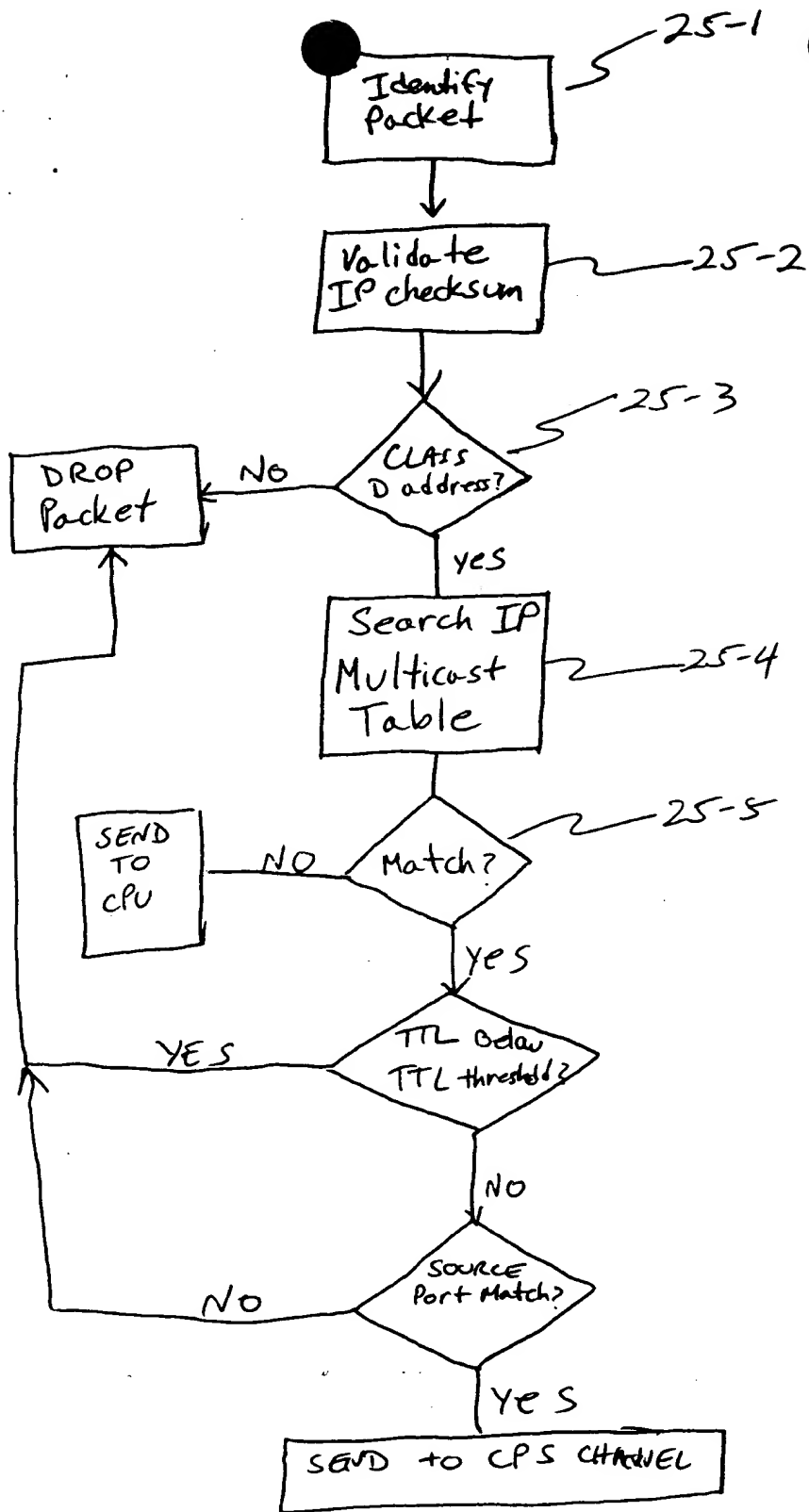


Fig. 25

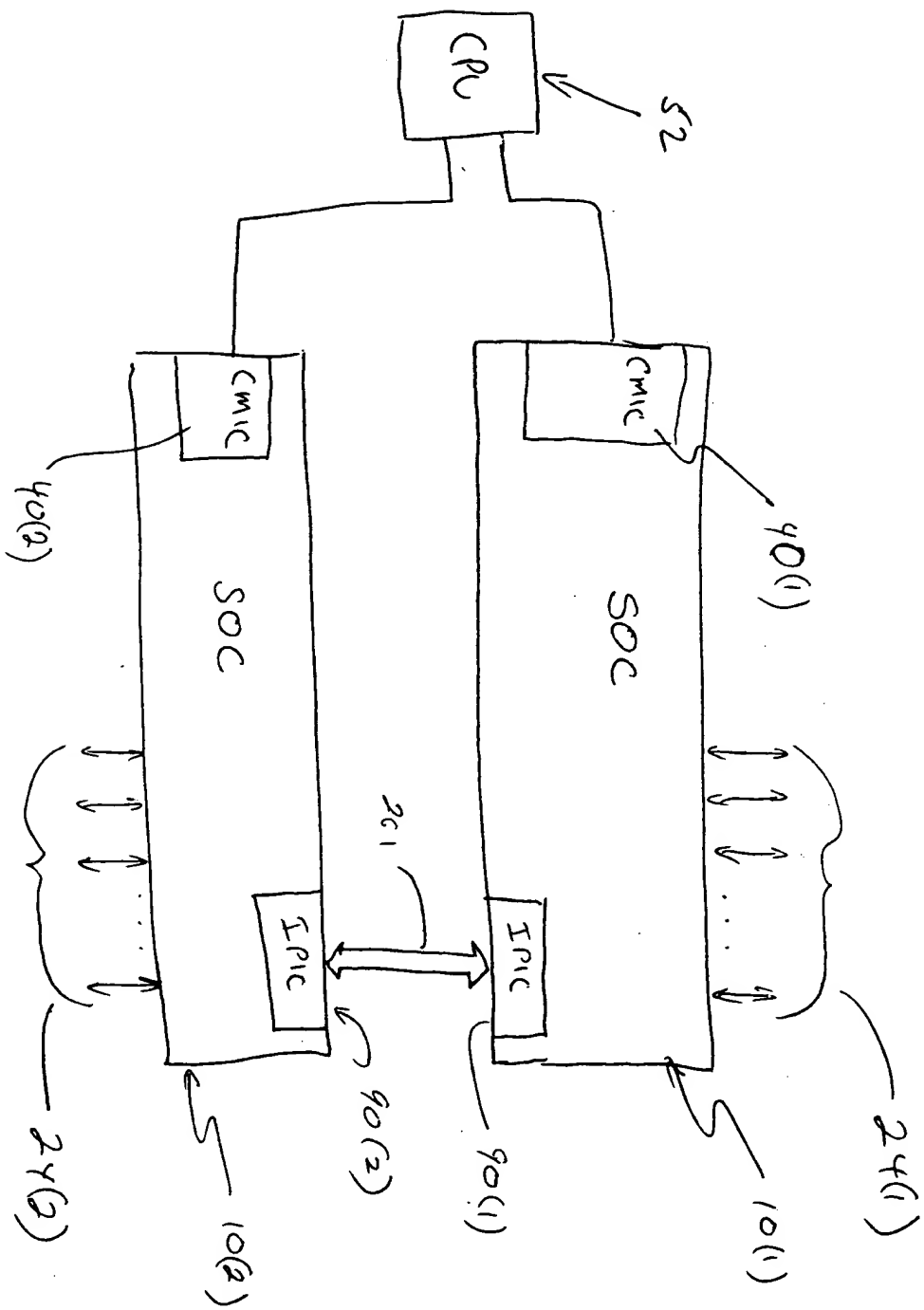


Fig. 26
09508157.034700

Fig. 27a

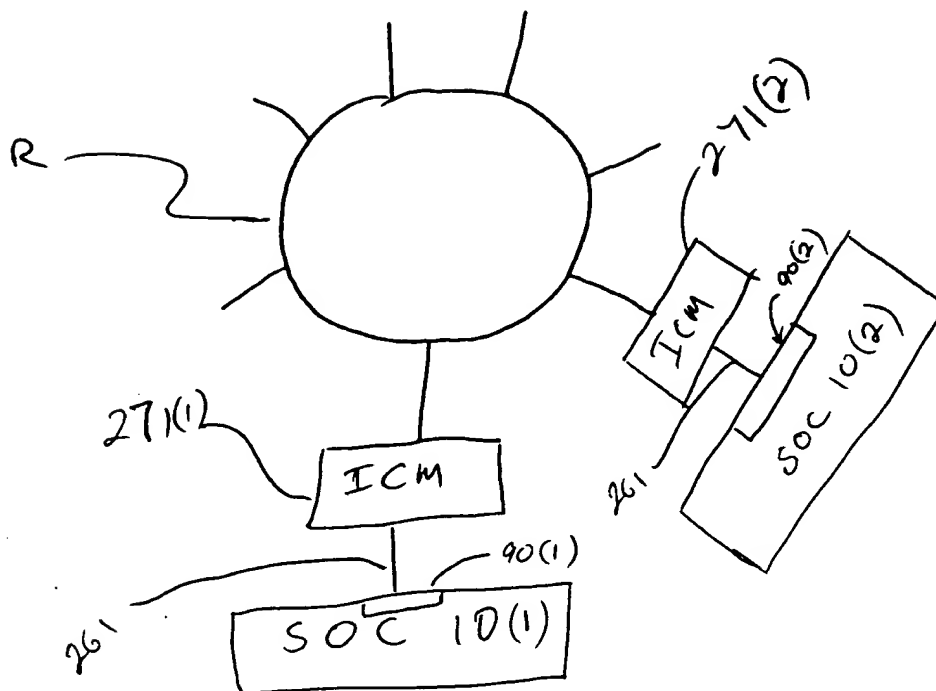
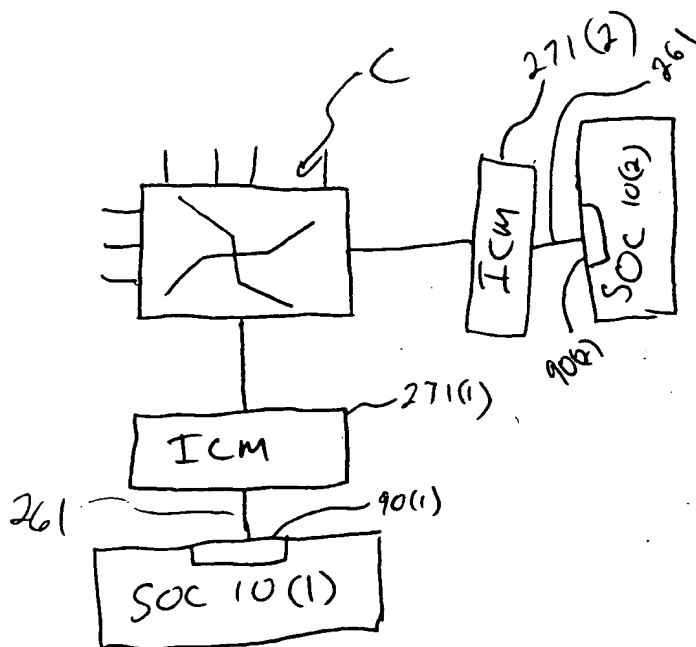


Fig. 27b



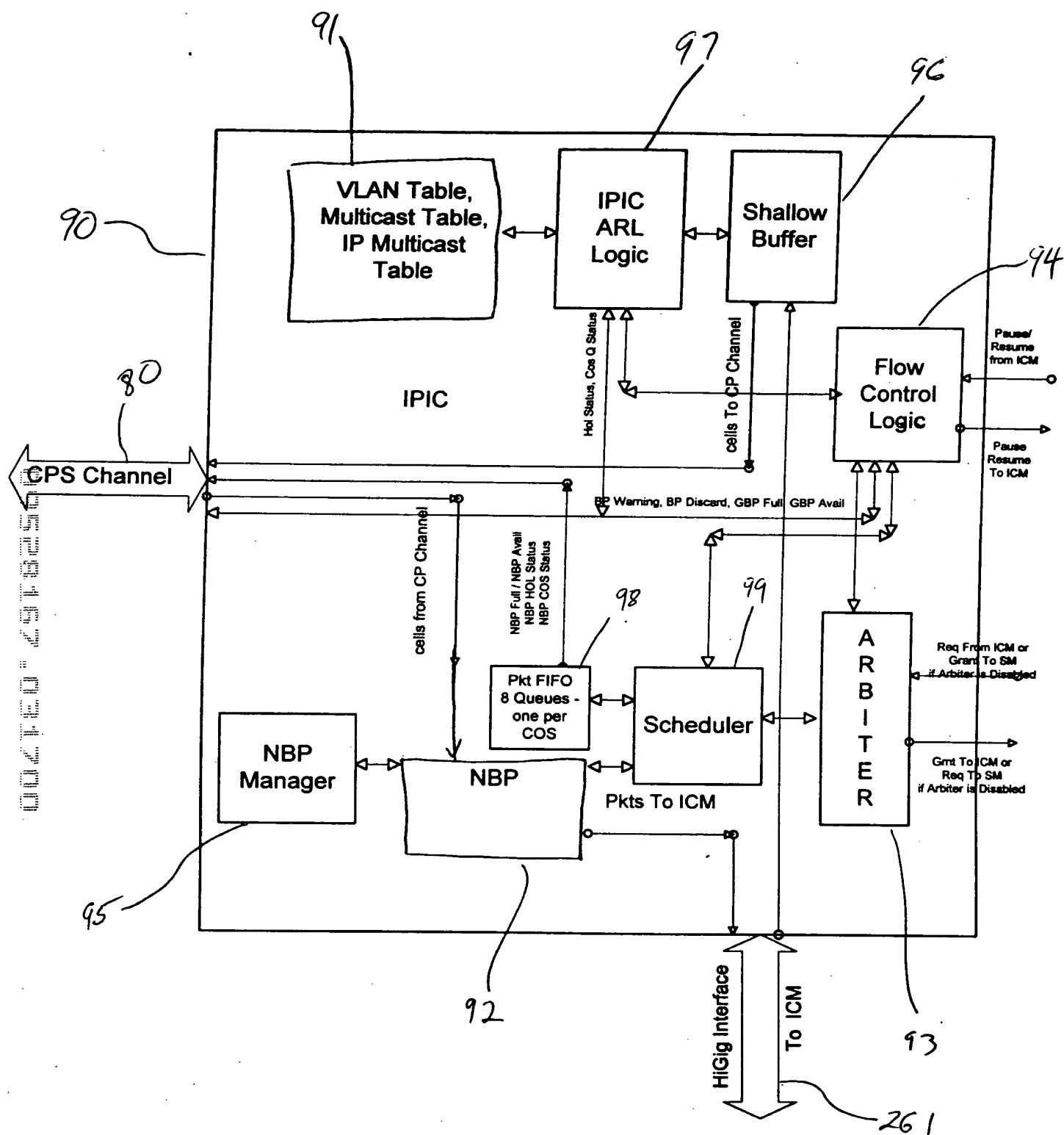


Fig 28.

002700 29122560

COS Queue (3b)	C P F	NCA (2b)	802.1p Priority (3b)	Rate Counter (8b)	Rate Counter Threshold (8b)	Rate Discard Thresho ld (8b)	New Code Point (6b)	New COS Queue (3b)	New 802.1 Priority (3b)
----------------------	-------------	-------------	----------------------------	-------------------------	--------------------------------------	---------------------------------------	------------------------------	-----------------------------	----------------------------------

FIGURE 30

Figure 1 consists of 12 histograms arranged in a 6x2 grid. The columns are labeled 'n=10' and 'n=20'. The rows are labeled 'm=10', 'm=20', 'm=30', 'm=40', 'm=50', and 'm=60'. The x-axis for all histograms is 'Number of non-zero elements' ranging from 0 to 100. The y-axis is 'Frequency' ranging from 0 to 10. The distributions are generally bell-shaped and centered around 50 for n=10 and around 100 for n=20. As m increases, the distributions become narrower and taller.

Offset Field	Offset 1	Offset 2	Offset 3	Offset 4
000	0-15	16-31	32-47	48-63
001	8-23	24-39	40-55	56-71
010	16-31	32-47	48-63	64-79
011	24-39	40-55	56-71	72-87
100	32-47	48-63	64-79	80-95
101	40-55	56-71	72-87	88-103
110	48-63	64-79	80-95	96-111
111	56-71	72-87	88-103	104-119

Figure 31

002400-2948660

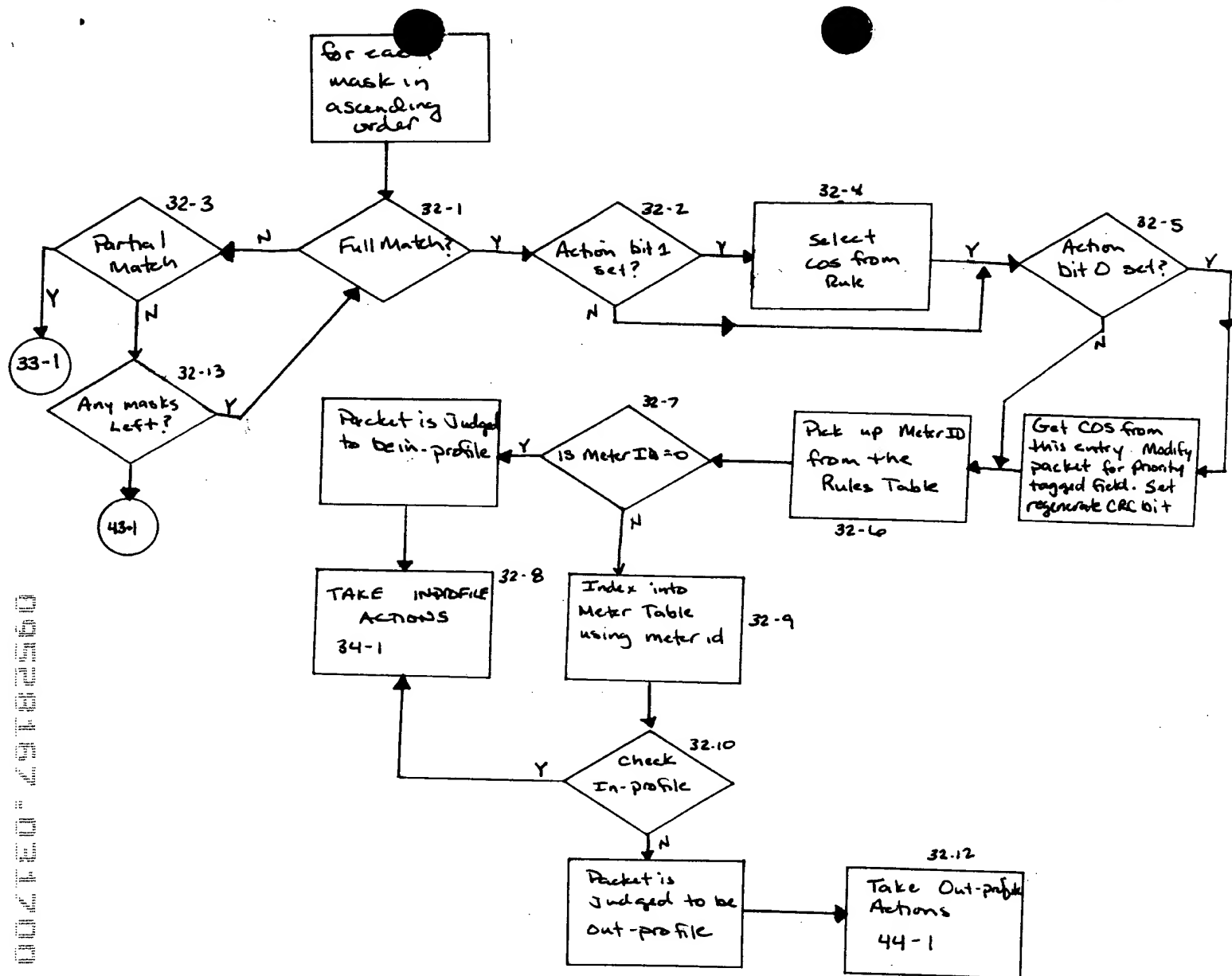


FIGURE 32

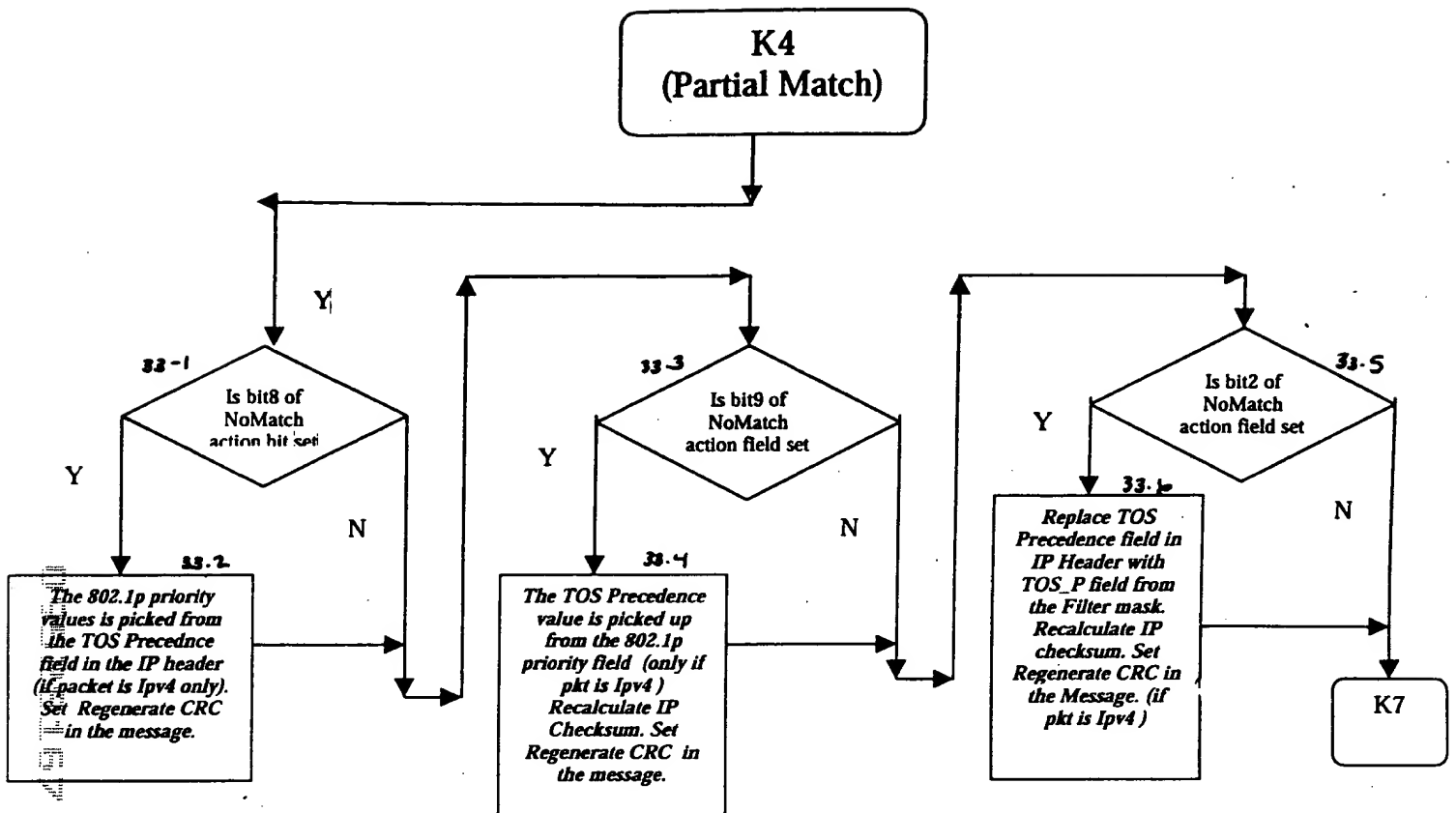


FIGURE 33

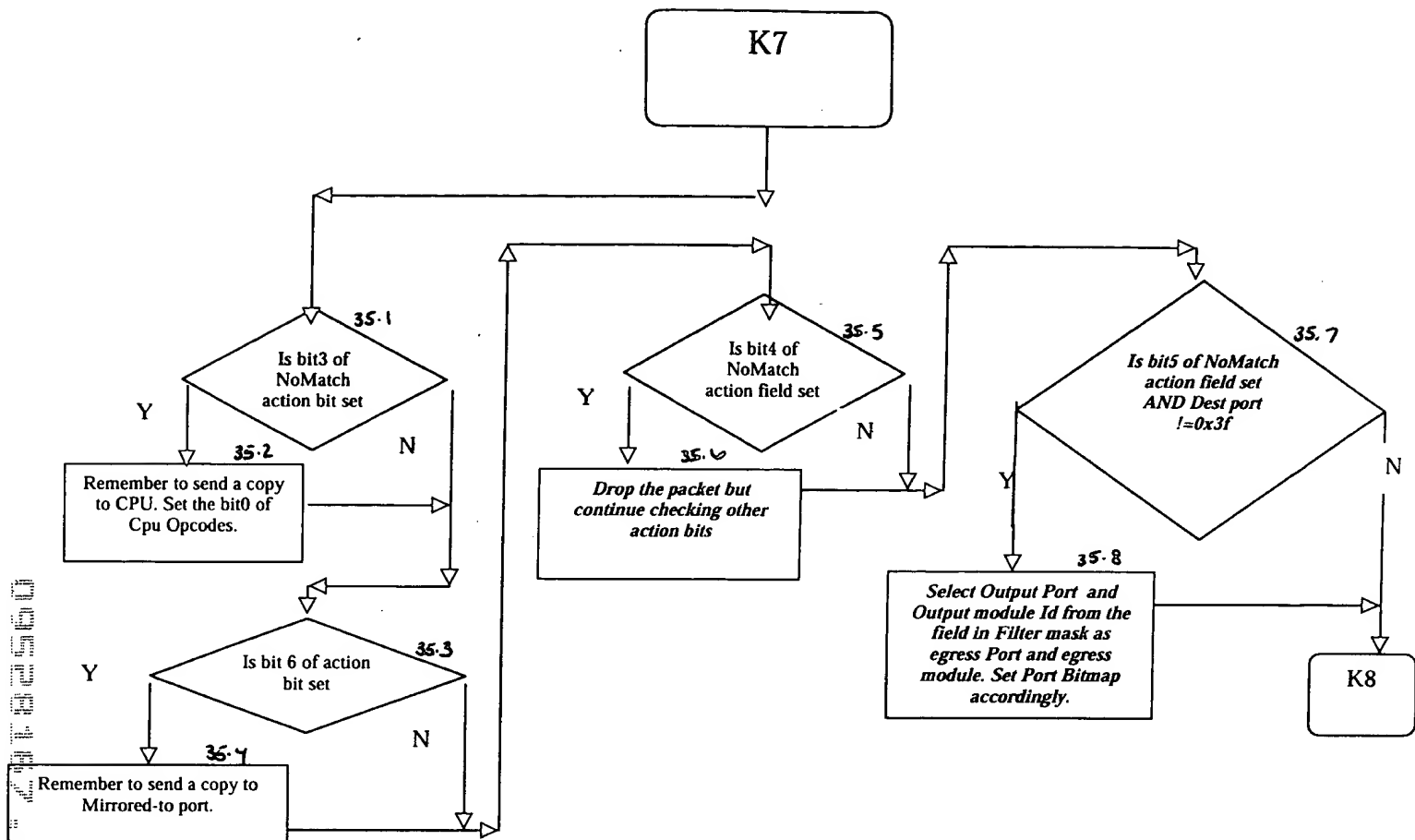


Figure 35

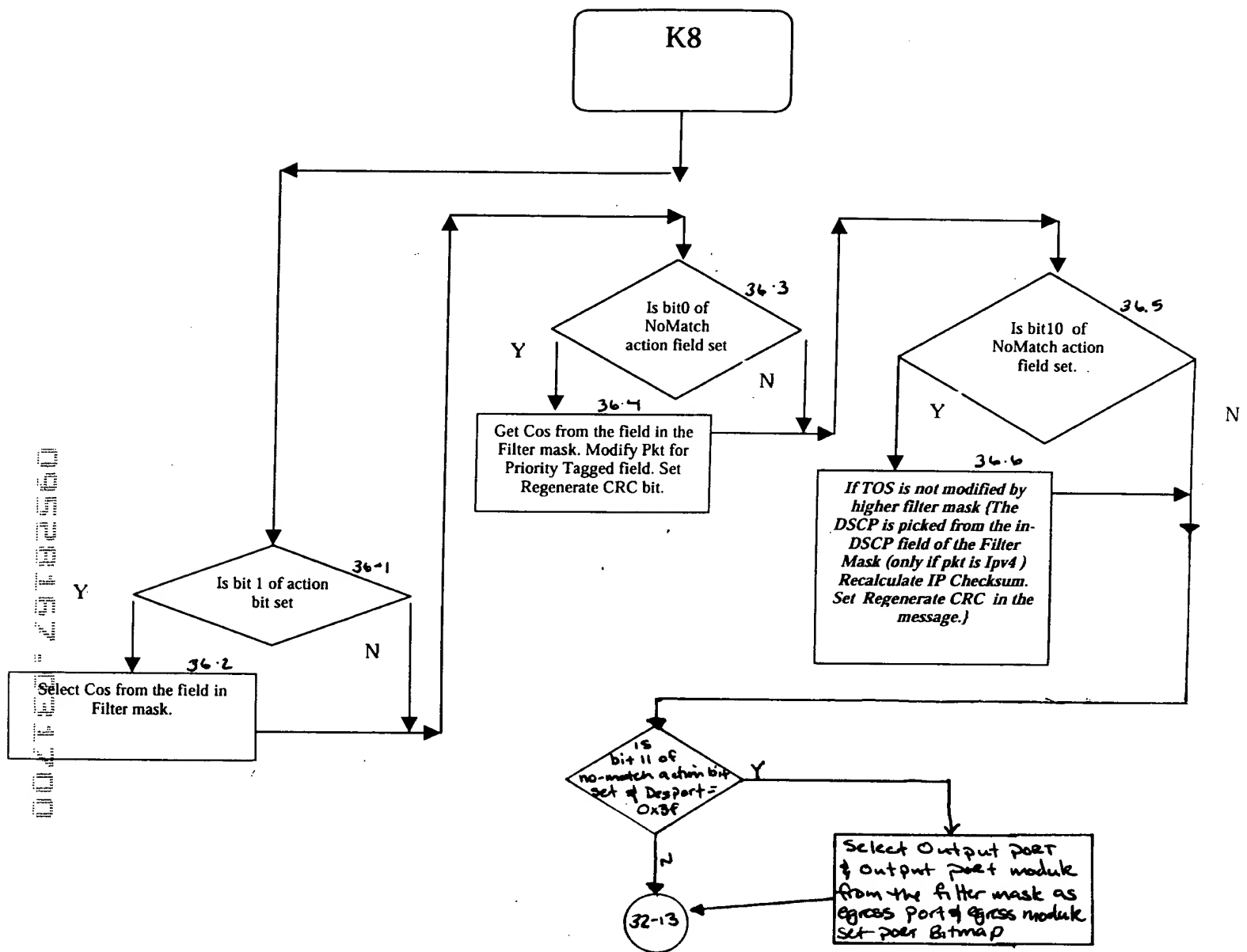


FIGURE 36

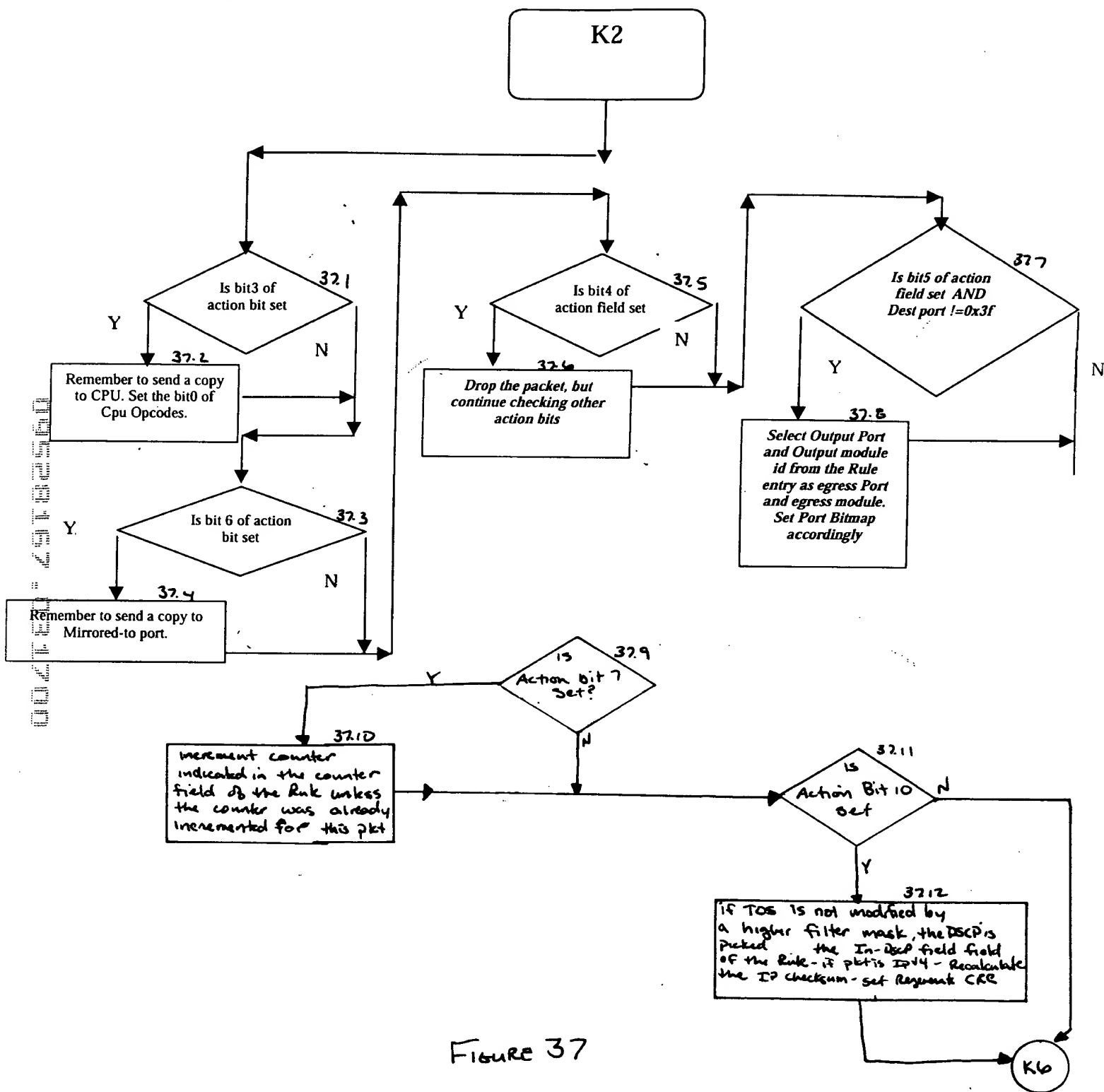


Figure 37

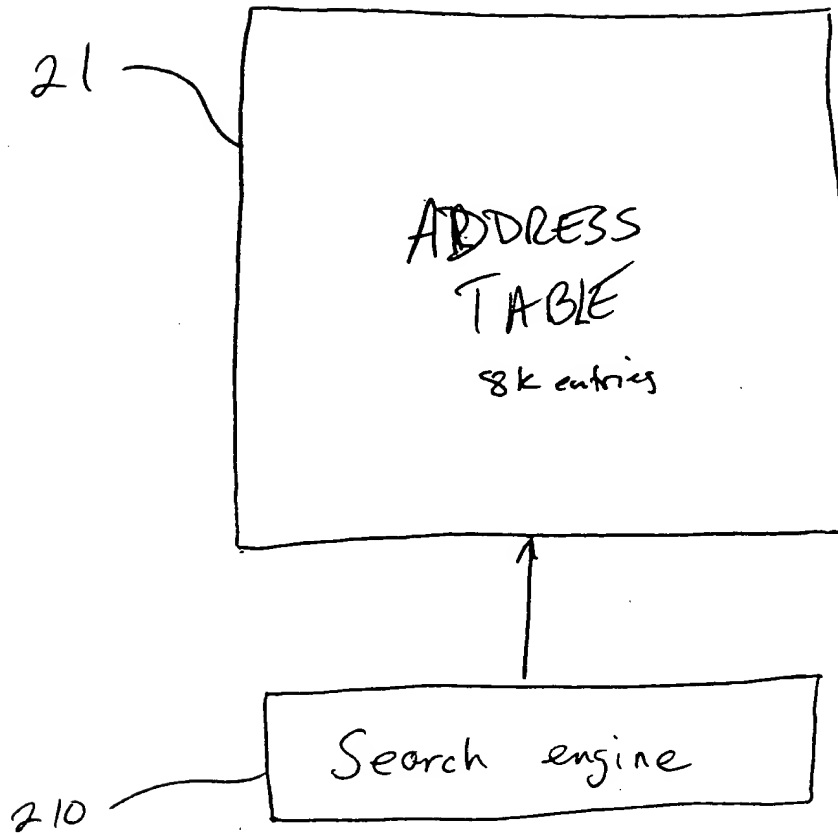


Fig 38

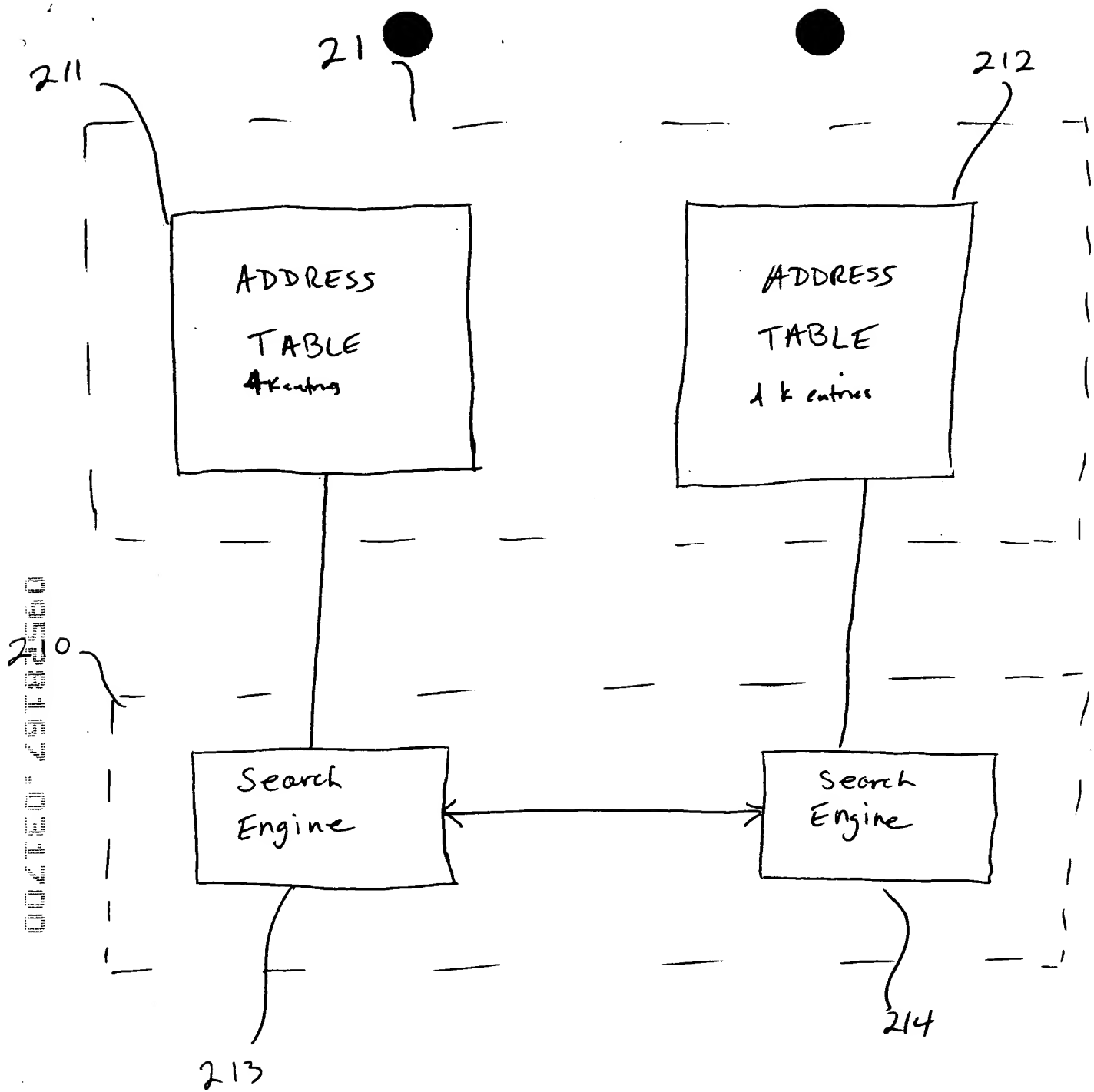
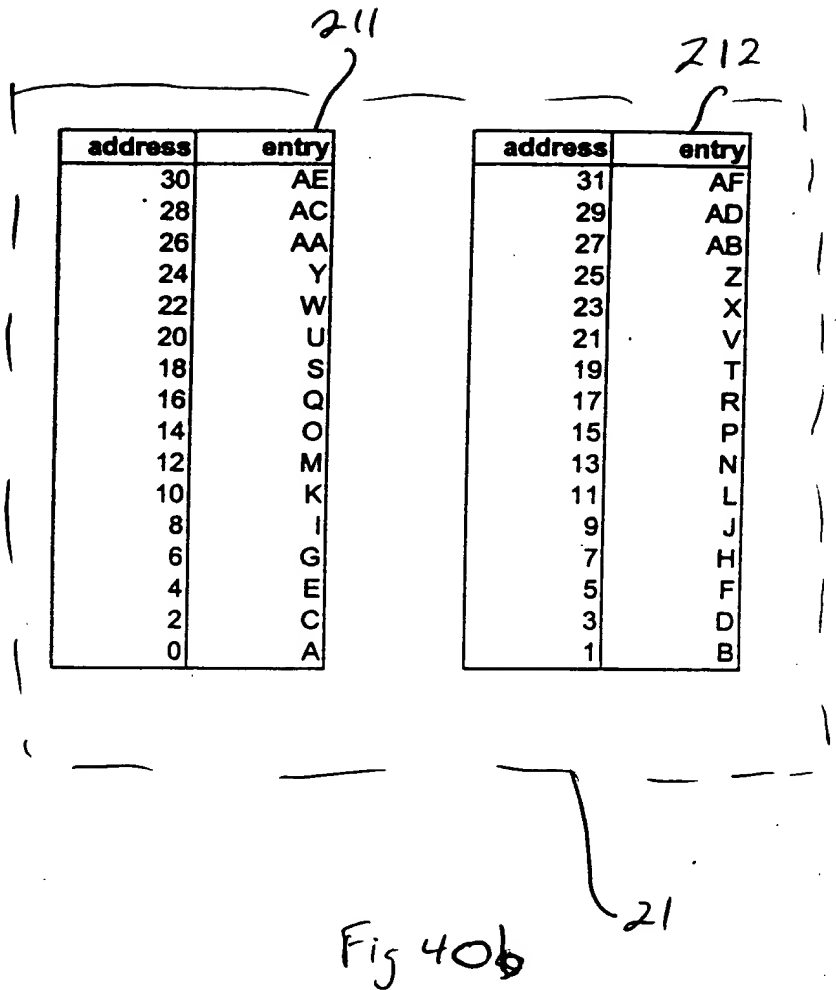


Fig. 39

Figure 40a

21 ~

address	entry
31	AF
30	AE
29	AD
28	AC
27	AB
26	AA
25	Z
24	Y
23	X
22	W
21	V
20	U
19	T
18	S
17	R
16	Q
15	P
14	O
13	N
12	M
11	L
10	K
9	J
8	I
7	H
6	G
5	F
4	E
3	D
2	C
1	B
0	A



21 —

address	entry
31	NN
30	MM
29	LL
28	KK
27	JJ
26	GH
25	CF
24	CC
23	BE
22	BD
21	BC
20	BA
19	AC
18	AB
17	AA
16	Y
15	X
14	V
13	T
12	S
11	R
10	Q
9	N
8	M
7	L
6	K
5	J
4	G
3	E
2	D
1	C
0	B

address	entry
30	MM
28	KK
26	GH
24	CC
22	BD
20	BA
18	AB
16	Y
14	V
12	S
10	Q
8	M
6	K
4	G
2	D
0	B

address	entry
31	NN
29	LL
27	JJ
25	CF
23	BE
21	BC
19	AC
17	AA
15	X
13	T
11	R
9	N
7	L
5	J
3	E
1	C

Fig 41a

[illegible]

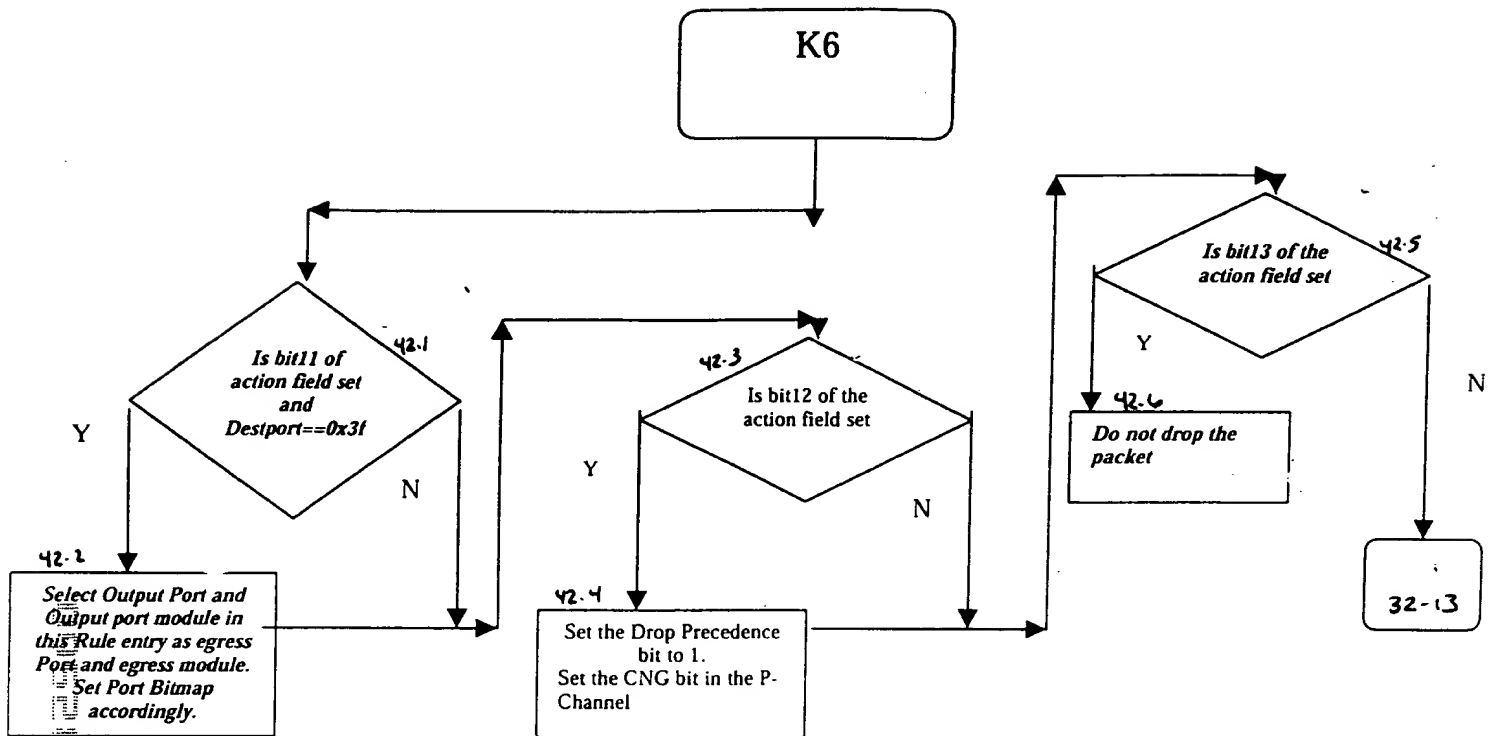


Figure 72

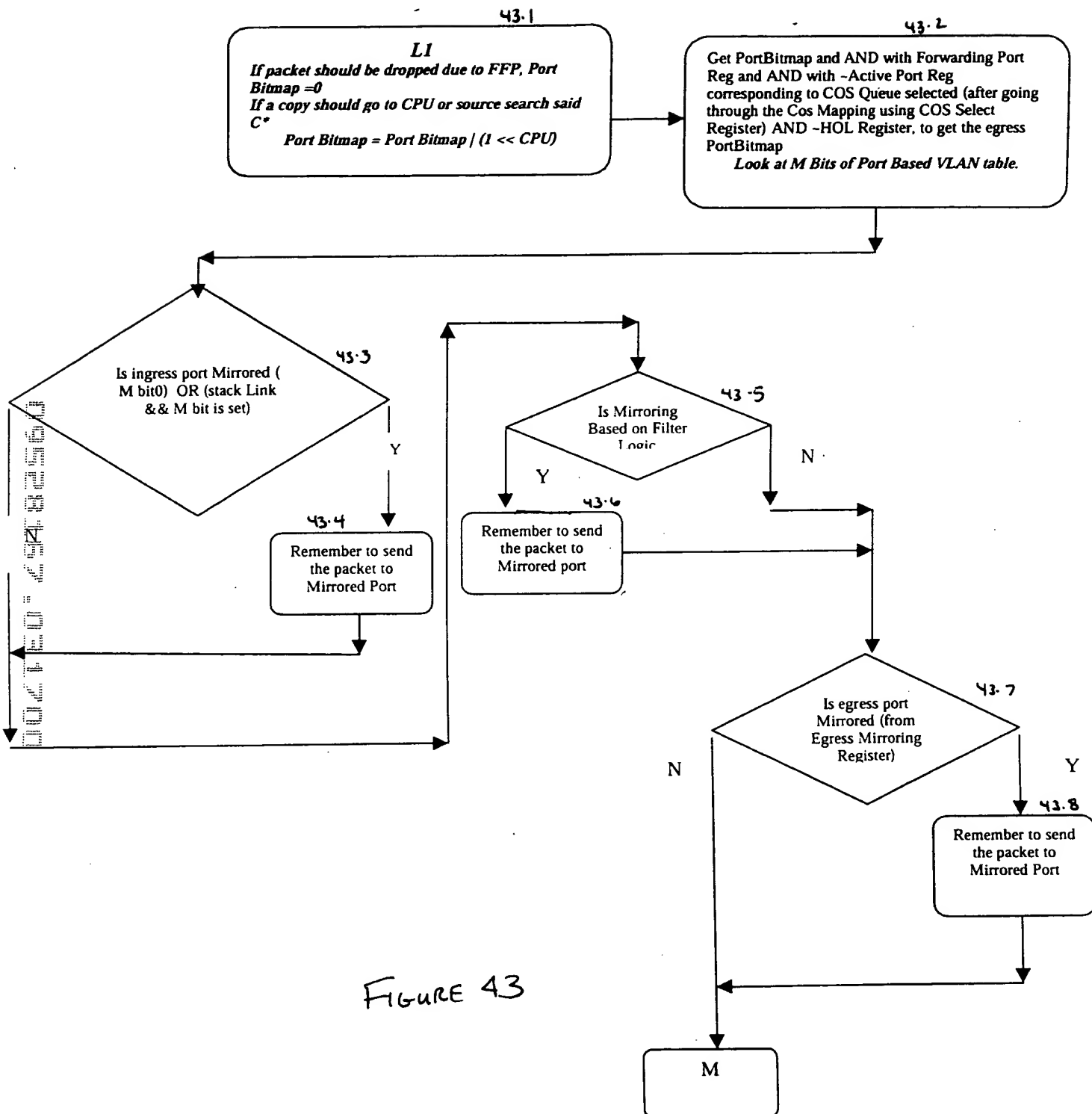


FIGURE 43

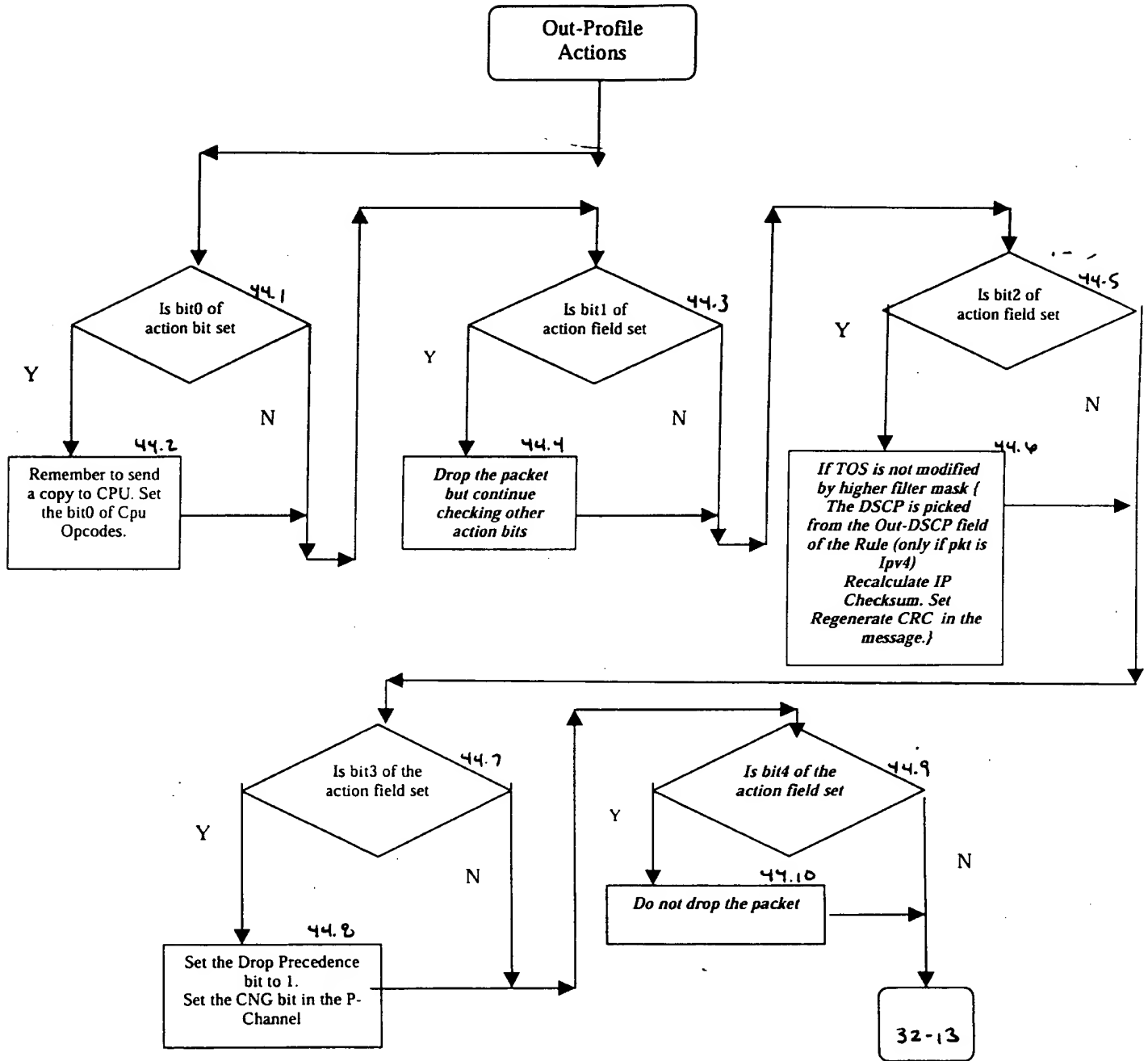


FIGURE 44

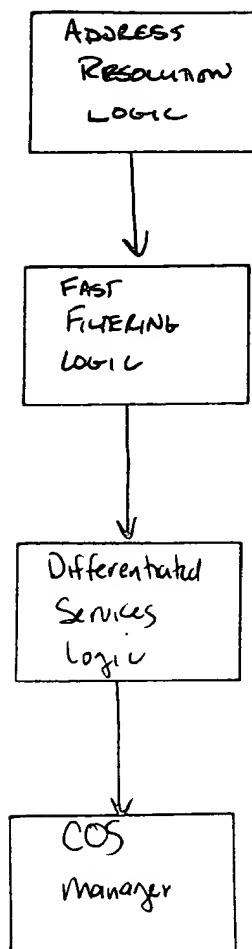


FIGURE 45

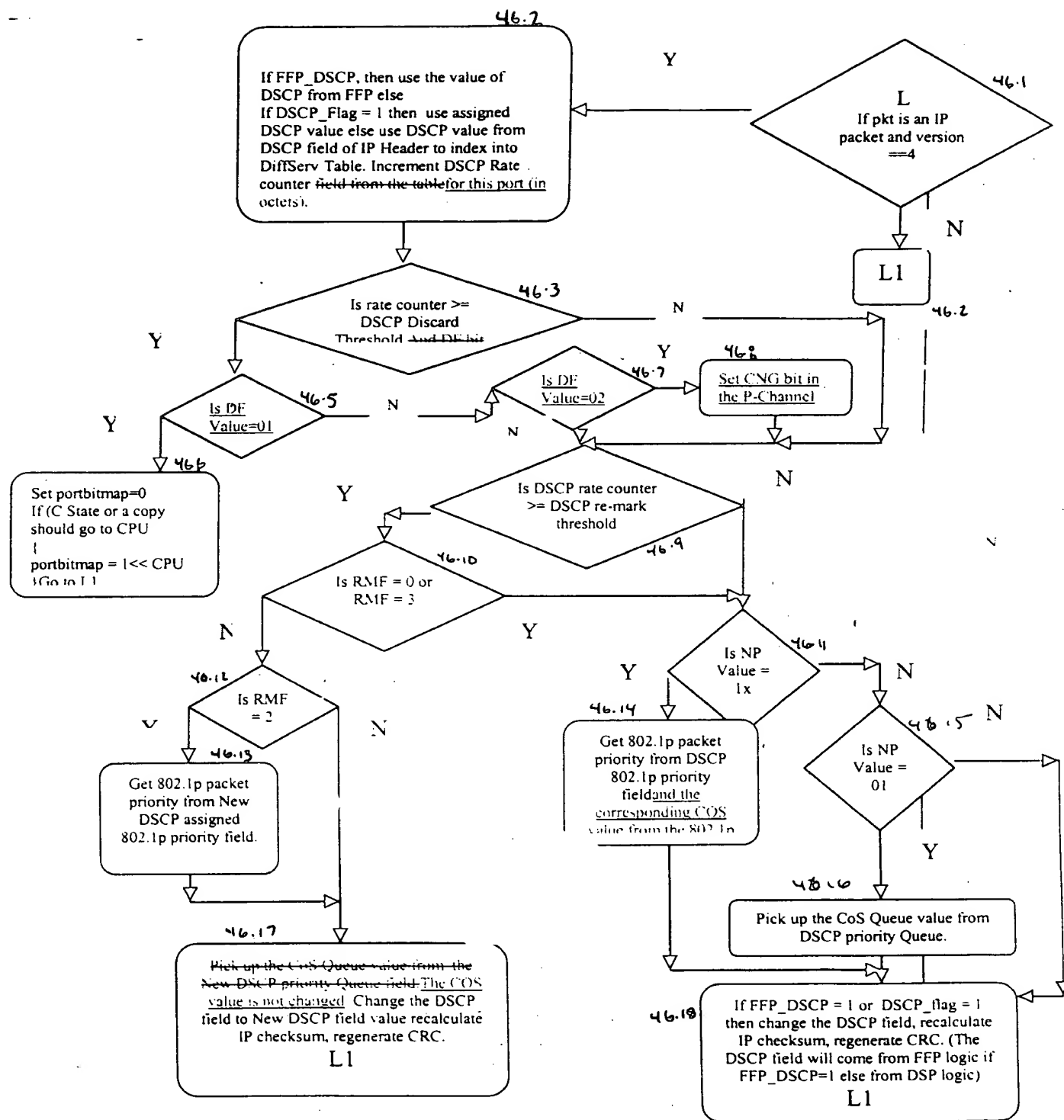


FIGURE 46

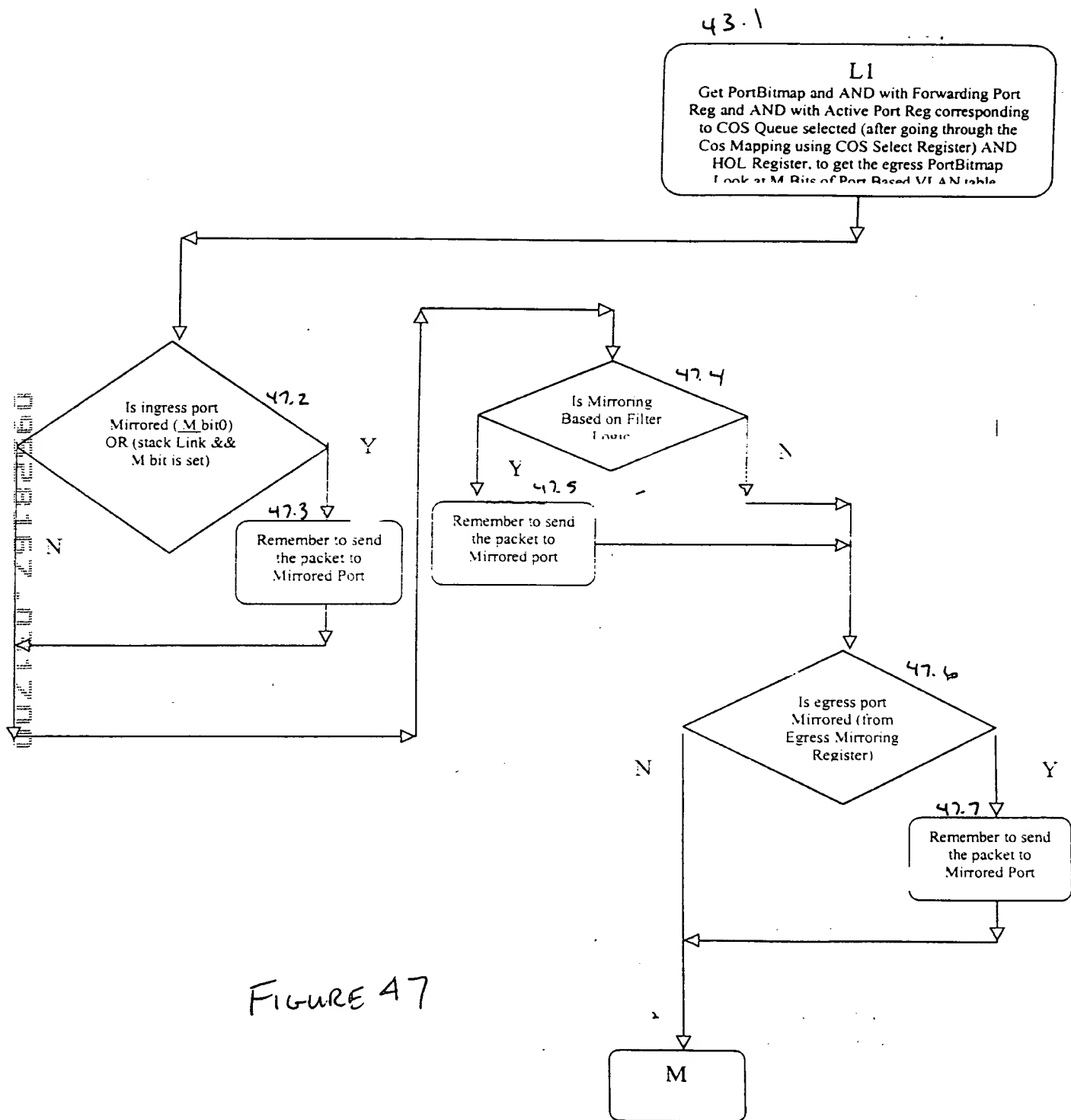


FIGURE 47

```

graph TD
    48.1[Initialize  
Lp = 0  
Lc = 0] --> 48.2[Packet Arrival]
    48.2 --> 48.3[Count++]
    48.3 --> 48.4{Count >= Threshold}
    48.4 -- Yes --> 48.5[Set switch bit]
    48.4 -- No --> 48.11[Select egress trunk por, i,  
according to Rules logic]
    48.5 --> 48.6[Count # of bits in  
the packet (Lc)]
    48.6 --> 48.7{Is the switch bit  
set?}
    48.7 -- No --> 48.8[Select egress trunk por, i,  
according to Rules logic]
    48.7 -- Yes --> 48.9{Lc + N >= Lp}
    48.9 -- No --> 48.10[Not a candidate for  
switching links]
    48.9 -- Yes --> 48.12((A))
    48.11 --> 48.1
    48.10 --> 48.1
    48.8 --> 48.1
    48.12 --> 48.1

```

48.1 Initialize
Lp = 0
Lc = 0

48.2 Packet Arrival

48.3 Count++

48.4 Count >= Threshold

Yes

48.5 Set switch bit

No

48.6 Count # of bits in the packet (Lc)

48.7 Is the switch bit set?

No

48.8 Select egress trunk por, i, according to Rules logic

Yes

48.9 Lc + N >= Lp

No

48.10 Not a candidate for switching links

Yes

48.12 A

48.11 Select egress trunk por, i, according to Rules logic

N - the number of bits accounts for IPG and preamble

Figure 48

FIGURE 48


```

graph TD
    A((A)) --> B[This frame is a candidate to switch the flow to another link]
    B --> C[Select the next port, j, according to the Rules logic]
    C --> D{lj + Lc > li}
    D -- Yes --> E[Queue the frame on link j]
    E --> F[Update the Rules logic to reflect the new mapping to port j]
    F --> G[Reset switch bit  
Reset count]
    G --> H((Done))
    D -- No --> I{Any more ports to check}
    I -- Yes --> C
    I -- No --> J[There are no more egress trunk ports that satisfy the criteria to switch flow to another link]
    J --> K[Queue this frame on link i]
    K --> L((Done))

```

Figure 49

10/100/1000 Mbps Switch

Gigabit link

Trunk Group of 10/100 Mbps links

Server

Workstation

FIGURE 50

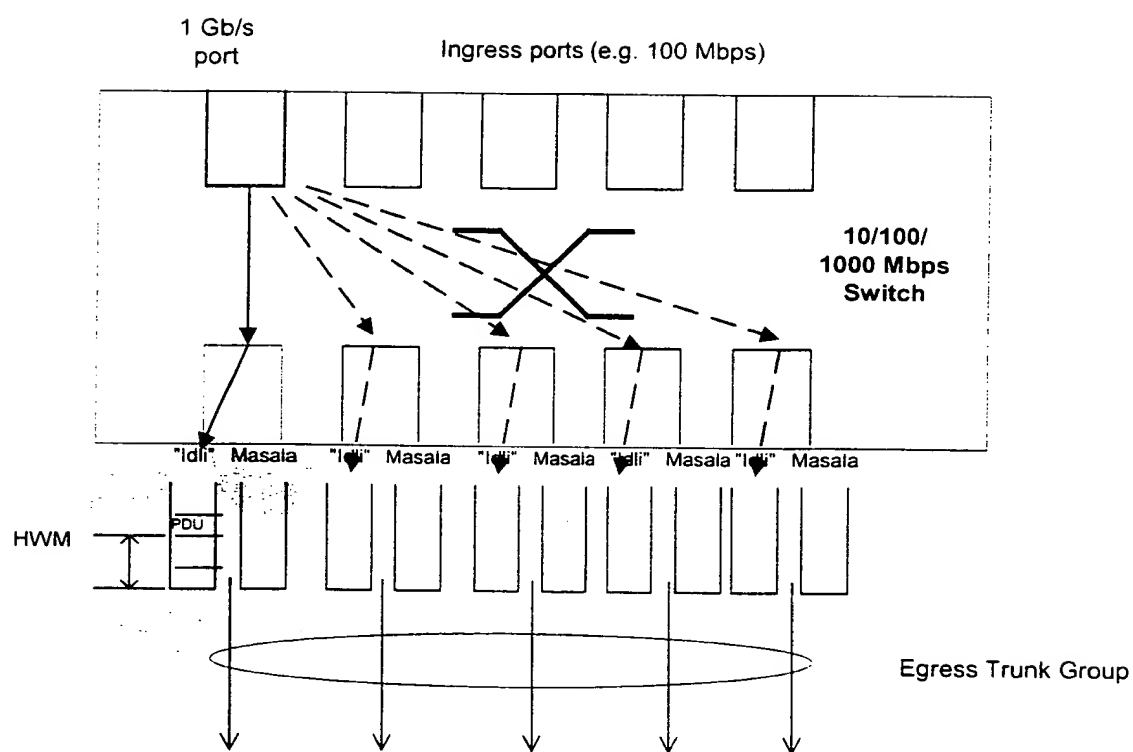


FIGURE 51

```

graph TD
    Start([Frame Arrival]) -- 52.1 --> Map[Map the frame to a trunk port]
    Map --> IsGigabit{Is the frame from Gigabit port}
    IsGigabit -- No --> Masala[Insert frame in the Masala queue]
    Masala --> Done1([Done])
    IsGigabit -- Yes --> Pkts{# of packets in the Idli buffer >= HWM}
    Pkts -- No --> Idli[Insert frame in the Idli queue]
    Idli --> Done2([Done])
    Pkts -- Yes --> PDB[Insert Marker PDB in the Idli buffer; Set the port unavailable for frames from the gigabit port]
    PDB -- 52.8 --> Select[Select the next available trunk port (e.g. round robin)]
    Select -- 52.9 --> Switch[Switch frame to the next egress trunk port buffer]
    Switch -- 52.10 --> Update[Update the mapping table]
    Update -- 52.11 --> Done3([Done])

```

FIGURE 52

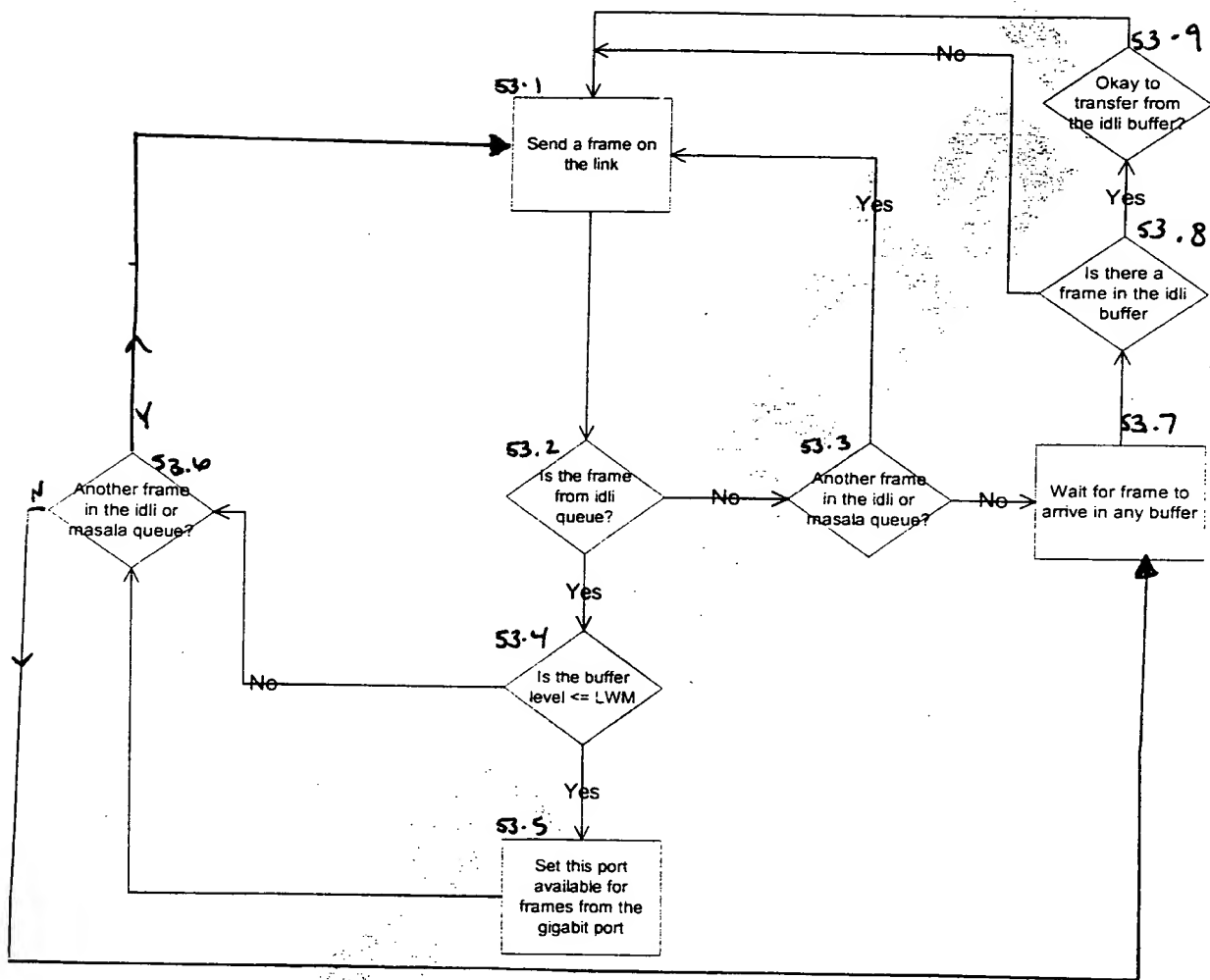


FIGURE 53

2 X 2 X 4 = 16 QUEUES

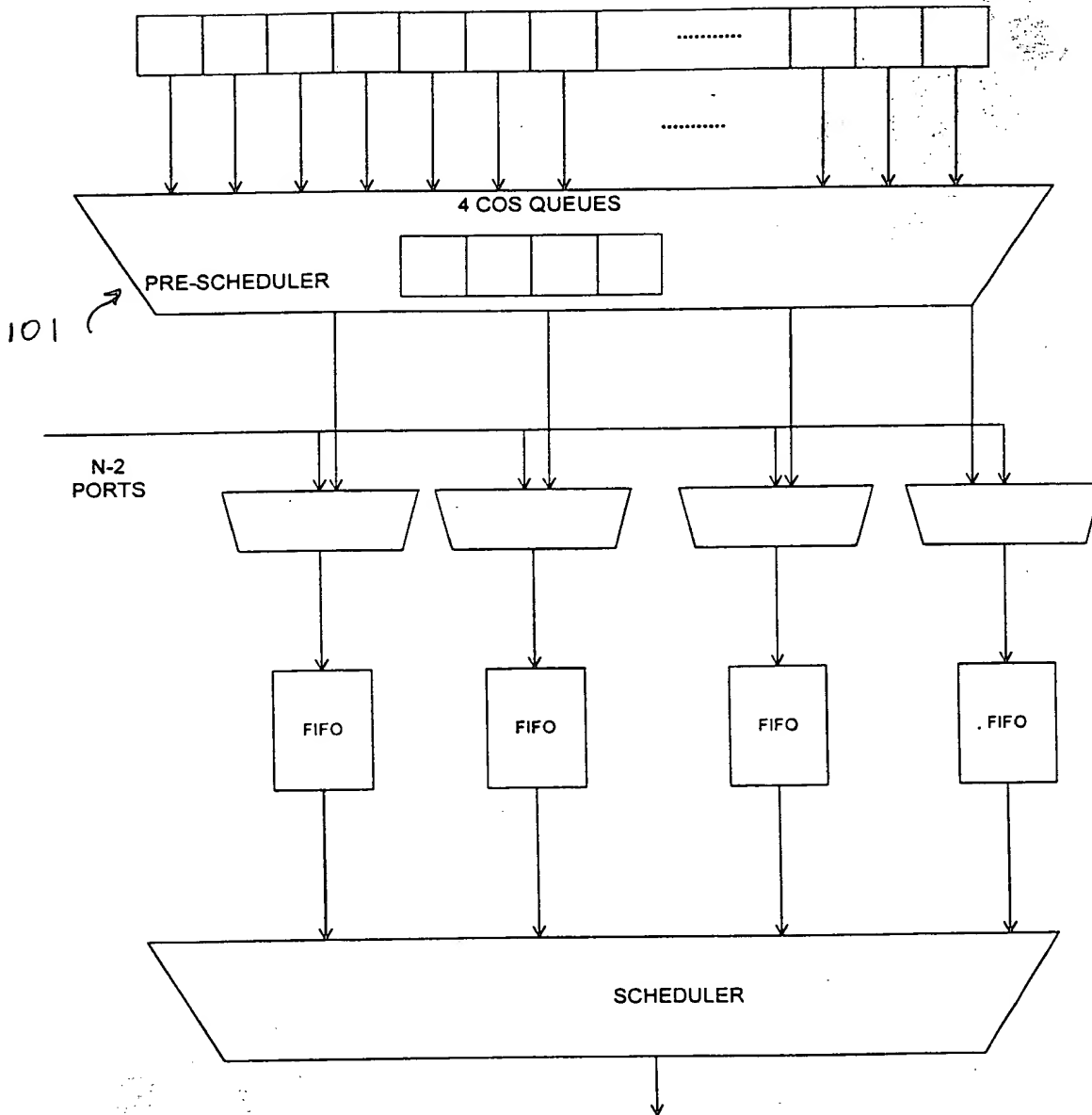


FIGURE 54

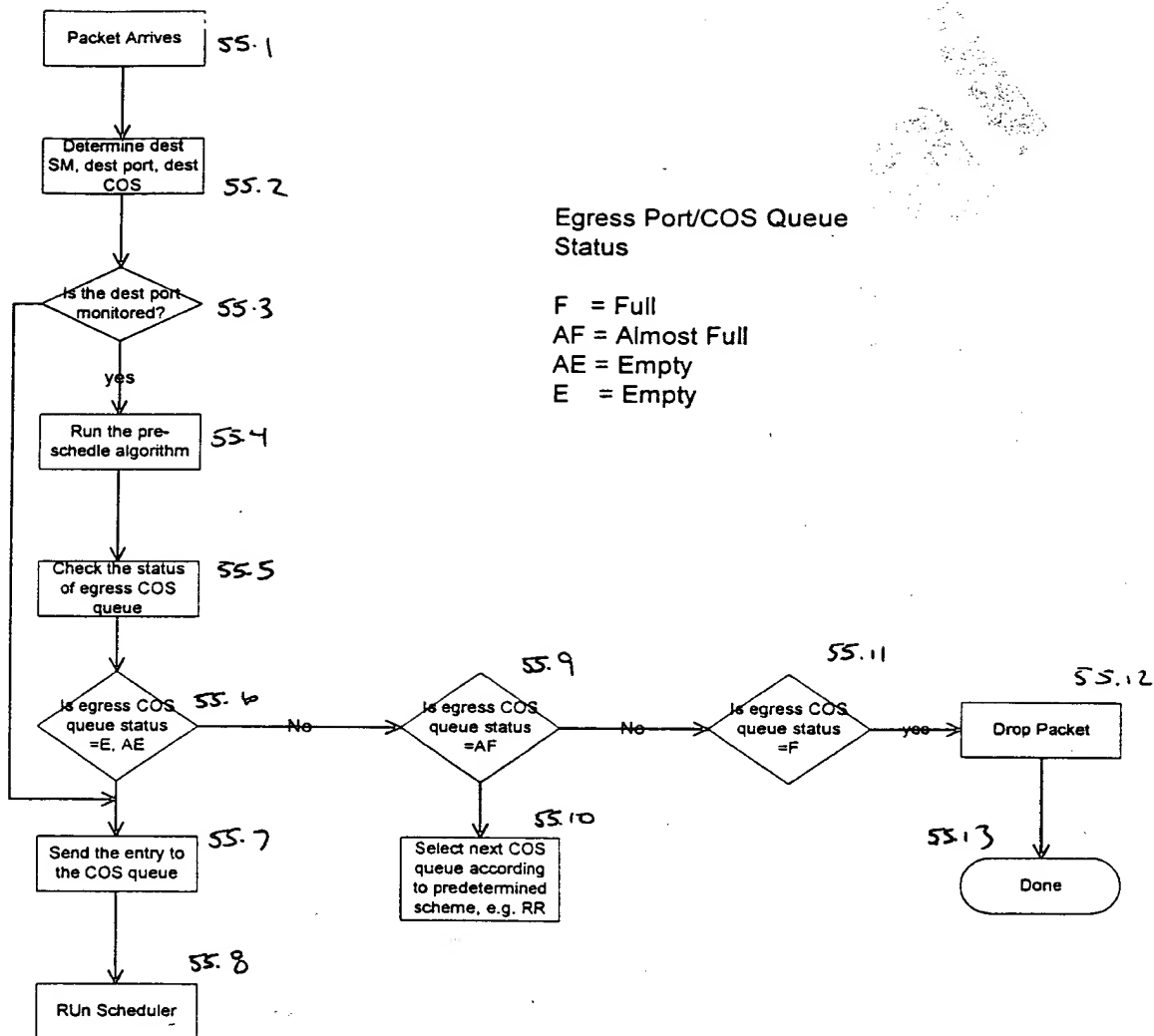


FIGURE 55

Diagram illustrating a packet scheduling algorithm (likely a priority-based or round-robin algorithm) using a crossbar switch and a queue.

The switch has six ports (1 to 6). Port 4 sends an indication to ingress port 1 to send a Marker Response PDU. Port 5 is crossed out, indicating it is not used for scheduling. Subsequent frames (A3, A4, A5) are queued to link 2.

Labels in the diagram include: "Indication to ingress port to send Marker Response PDU", "Ingress Port 1 sends out Marker Response PDU after getting indication from port 5", "Response PDU", "Subsequent frames from the flow are queued to link 2", and frame identifiers A1, A2, A3, A4, A5.

FIGURE 5b

Ingress Port 1
sends out Marker
Response PDU
after getting
indication from port.
5

Response
POU

Subsequent frames from the flow are queued to link 2